



Comisión Interministerial de Ciencia y Tecnología

Curriculum vitae

Eduard Ayguadé Parra

Marzo de 2011

Apellidos: Ayguadé Parra
DNI: 38428764L

Fecha de nacimiento : 15/05/1962

Nombre: Eduard
Sexo: V

Situación profesional actual

Organismo: Universitat Politècnica de Catalunya
Facultad / Escuela: Escola Tècnica Superior de Ingenieria Telecomunicacions / Facultat d'Informàtica de Barcelona
Depto./Secc./Unidad estr.: Departamento de Arquitectura de Computadores
Dirección postal: Jordi Girona 1-3, Campus Nord Mòdul D6. 08034 - Barcelona

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Especialización (Códigos UNESCO): 330406

Categoría profesional: Catedrático de Universidad

Fecha de inicio: 10/02/1997

Situación administrativa

Plantilla Contratado Interino Becario
 Otras situaciones especificar:

Dedicación A tiempo completo
A tiempo parcial

Líneas de investigación

Breve descripción, por medio de palabras claves, de la especialización y líneas de investigación actuales.
Arquitecturas de Altas Prestaciones, Compiladores y Entornos de Ejecución para Multiprocesadores

Formación Académica

Titulación Superior	Centro	Fecha
Ingeniero en Telecomunicaciones	ETSE de Telecomunicació Barcelona	Julio 1986

Doctorado	Centro	Fecha
Doctor en Telecomunicaciones	Universitat Politècnica de Catalunya	Octubre 1989

Actividades profesionales

Puesto	Institución	Fechas
Profesor Colaborador en Exclusiva	Facultat d'Informàtica de Barcelona (UPC)	10/86 a 9/87
Asociado a tiempo completo	Facultat d'Informàtica de Barcelona (UPC)	10/87 a 7/90
Titular de Escuela Universitaria	Facultat d'Informàtica de Barcelona (UPC)	7/90 a 12/90
Profesor Titular de Universidad	Escola Tècnica Superior Ingenieria en Telecomunicacions	12/90 a 2/97
Profesor Catedrático de Universidad	Escola Tècnica Superior Ingenieria en Telecomunicacions	desde 3/97

Idiomas (R = regular, B = bien, C = correctamente)

Idioma	Habla	Lee	Escribe
Inglés	C	C	C
Francés	R	R	R

Participación en Proyectos de I+D financiados en convocatorias públicas.
(nacionales y/o internacionales)

- TITULO DEL PROYECTO: Diseño de Arquitecturas Paralelas de Alta Velocidad a Bajo Coste (PA85-0314)
ENTIDAD FINANCIADORA: CAICYT
DURACION DESDE: 1985 HASTA: 1989
INVESTIGADOR PRINCIPAL: M. Valero

- TITULO DEL PROYECTO: Sistema Multiprocesador Fuertemente Acoplado
ENTIDAD FINANCIADORA: Institut Català de Tecnologia
DURACION DESDE: 1989 HASTA: 1991
INVESTIGADOR PRINCIPAL: J. Labarta

- TITULO DEL PROYECTO: Arquitectura, Herramientas de Programación y Sistemas Operativos para Multiprocesadores (TIC 89-392)
ENTIDAD FINANCIADORA: CICYT
DURACION DESDE: 1990 HASTA: 1992
INVESTIGADOR PRINCIPAL: J. Labarta

- TITULO DEL PROYECTO: Arquitectura y Compilación para Supercomputadores (TIC 92-880)
ENTIDAD FINANCIADORA: CICYT
DURACION DESDE: Julio 1992 HASTA: Junio 1995
INVESTIGADOR PRINCIPAL: M. Valero (E. Ayguadé: Responsable de la Tarea T2.2 “Transformaciones Automáticas” del Módulo M2 “Compiladores”)

- TITULO DEL PROYECTO: Acción Integrada Hispano-Británica 1995-1996: Distribución Automática de Datos para Computadores de Altas Prestaciones (HB163-B)
ENTIDAD FINANCIADORA: Ministerio de Educación y Ciencia
DURACION DESDE: Abril 1995 HASTA: Marzo 1996
INVESTIGADOR PRINCIPAL: M. Valero (E. Ayguadé: Responsable Técnico de la Propuesta)

- TITULO DEL PROYECTO: Computación de Altas Prestaciones (TIC 95-429)
ENTIDAD FINANCIADORA: CICYT
DURACION DESDE: Julio 1995 HASTA: Junio 1998
INVESTIGADOR PRINCIPAL: M. Valero (E. Ayguadé: Responsable del Módulo M2 “Compiladores”)

- TITULO DEL PROYECTO: Computación Paralela para el Modelado de Problemas Industriales (TIC96-1630-CE)
ENTIDAD FINANCIADORA: CICYT
DURACIÓN DESDE: Septiembre 1996 HASTA: Septiembre 1999
INVESTIGADOR PRINCIPAL: E. Ayguadé

- TITULO DEL PROYECTO: Acción Integrada Hispano-Austriaca 1997: Técnicas Avanzadas para la Distribución de Datos (HU1996-10)
ENTIDAD FINANCIADORA: Ministerio de Educación y Cultura
DURACION DESDE: Enero 1997 HASTA: Diciembre 1997
INVESTIGADOR PRINCIPAL: E. Ayguadé

- TITULO DEL PROYECTO: Explotación de Paralelismo y Multiprogramación (TIC97-1445-CE)
ENTIDAD FINANCIADORA: CICYT
DURACIÓN DESDE: Junio 1997 HASTA: Mayo 2000
INVESTIGADOR PRINCIPAL: E. Ayguadé

- TITULO DEL PROYECTO: Acció Integrada ACI amb University of Illinois 1997: High Performance Compilers
 ENTIDAD FINANCIADORA: CIRIT
 DURACION DESDE: Enero 1997 HASTA: Diciembre 1997
 INVESTIGADOR PRINCIPAL: E. Ayguadé
- TITULO DEL PROYECTO: Acció Integrada ACI amb University of California at Santa Cruz 1998: Multithreaded Architectures
 ENTIDAD FINANCIADORA: CIRIT
 DURACION DESDE: Enero 1998 HASTA: Diciembre 1998
 INVESTIGADOR PRINCIPAL: E. Ayguadé
- TITULO DEL PROYECTO: Acció Integrada ACI amb University of Illinois 1998: High Performance Compilers for Multithreaded Architectures
 ENTIDAD FINANCIADORA: CIRIT
 DURACION DESDE: Enero 1998 HASTA: Diciembre 1998
 INVESTIGADOR PRINCIPAL: E. Ayguadé
- TITULO DEL PROYECTO: Computación de Altas Prestaciones II (TIC 98-511)
 ENTIDAD FINANCIADORA: CICYT
 DURACION DESDE: Julio 1998 HASTA: Junio 2001
 INVESTIGADOR PRINCIPAL: M. Valero
- TITULO DEL PROYECTO: Proyecto Complementatio INTONE (TIC2000-2909-CE)
 ENTIDAD FINANCIADORA: CICYT
 DURACION DESDE: Noviembre 2000 HASTA: Abril 2003
 INVESTIGADOR PRINCIPAL: E. Ayguadé
- TITULO DEL PROYECTO: Computación de Altas Prestaciones III (TIC2001-0995)
 ENTIDAD FINANCIADORA: CICYT
 DURACION DESDE: Julio 2001 HASTA: Junio 2004
 INVESTIGADOR PRINCIPAL: M. Valero
- TITULO DEL PROYECTO: Computación de Altas Prestaciones IV (TIN2004-07739-C02-01)
 ENTIDAD FINANCIADORA: CICYT
 DURACION DESDE: Julio 2004 HASTA: Diciembre 2007
 INVESTIGADOR PRINCIPAL: M. Valero
- TITULO DEL PROYECTO: Supercomputación y eCiencia (CSD2007-00050)
 ENTIDAD FINANCIADORA: MICINN Consolider
 DURACION DESDE: Noviembre 2007 HASTA: Septiembre 2012
 INVESTIGADOR PRINCIPAL: M. Valero
- TITULO DEL PROYECTO: Computación de Altas Prestaciones V (TIN2007-60625)
 ENTIDAD FINANCIADORA: CICYT Consolider
 DURACION DESDE: Octubre 2007 HASTA: Agosto 2012
 INVESTIGADOR PRINCIPAL: M. Valero
- TITULO DEL PROYECTO: Grup de Recerca Consolidat MPEXP (Models de Programació i Entorns d'Execució Paral.lels) (2009 SGR980)
 ENTIDAD FINANCIADORA: Generalitat de Catalunya, Dep. d'Innovació, Universitats i Empresa
 DURACION DESDE: Octubre 2009 HASTA: Septiembre 2012
 INVESTIGADOR PRINCIPAL: E. Ayguadé

Publicaciones o Documentos Científico-Técnicos

(CLAVE: CL=capítulo de libro, A=artículo en revista, Conf =artículo en proceedings de conferencia)

- AUTORES: T. Jové, E. Ayguadé y M. Valero
TITULO: Arquitectura de los Microprocesadores CISC de 32 bits
REF. REVISTA/LIBRO: Mundo Electrónico, Ed. Marcombo. No.180, pp. 25-34. Enero 1988
CLAVE: A
- AUTORES: J. Labarta and E. Ayguadé
TITULO: GTS: Extracting Full Parallelism Out of DO Loops
REF. REVISTA/LIBRO: PARLE'89, Parallel Architectures and Languages Europe. Lectures in Computer Science, Springer-Verlag. No. 366, pp. 43-54. June 1989.
ISSN/ISBN: 0302-9743
CLAVE: A
- AUTORES: E. Ayguadé and J. Labarta, J. Torres and P. Borensztein
TITULO: GTS: Parallelization and Vectorization of Tight Recurrences
REF. REVISTA/LIBRO: Supercomputing'89, Reno-Nevada. pp. 531-539. November 1989
CLAVE: Conf
- AUTORES: E. Ayguadé, J. Labarta, J. Torres, J.M. Llaberia and M. Valero
TITULO: Parallelism Evaluation and Partitioning of Nested Loops for Shared-Memory Multiprocessors
REF. REVISTA/LIBRO: Third Workshop on Languages and Compilers for Parallel Computing, Irvine (CA), August 1990.
También en Advances in Programming Languages and Compilers for Parallel Computing, A. Nicolau et al. (Eds.), chapter 11 (pp. 220-242), Pitman/The MIT Press, 1991
ISSN/ISBN: 0273088416
CLAVE: CL
- AUTORES: E. Ayguadé, J. Labarta, J. Torres, J.M. Llaberia and M. Valero
TITULO: Nested-Loop Partitioning for Shared-Memory Multiprocessor Systems
REF. REVISTA/LIBRO: 2nd International Workshop on Compilers for Parallel Computers, Paris (France). pp. 378-385. December 1990
También en 1^{as} Jornadas de Paralelismo, Sitges-Barcelona (España). Septiembre 1990.
CLAVE: Conf
- AUTORES: R. Badia, J. Cortadella and E. Ayguadé
TITULO: Computer-aided Synthesis of Data-path by Using a Simulated-annealing-based Approach
REF. REVISTA/LIBRO: 9th International Symposium on Applied Informatics, Innsbruck, Austria. pp. 326-329. February 1991
CLAVE: Conf
- AUTORES: J. Torres, E. Ayguadé, J. Labarta, J.M. Llaberia, and M. Valero
TITULO: Automatic Data-Mapping for Distributed-Memory Multiprocessors Systems
REF. REVISTA/LIBRO: 9th International Symposium on Applied Informatics, Innsbruck, Austria. pp. 347-350. February 1991
CLAVE: Conf

- AUTORES: J. Cortadella, R. Badia and E. Ayguadé,
 TITULO: Scheduling in a Continuous Area-Time Design Space: A Simulated-annealing-based Approach
 REF. REVISTA/LIBRO: 5th Int. Workshop on High-level Synthesis, Germany. pp. 102-117. March 1991
 CLAVE: Conf
- AUTORES: J. Torres, E. Ayguadé, J. Labarta, J.M. Llaberia and M. Valero,
 TITULO: On Automatic Loop Data-Mapping for Distributed-Memory Multiprocessors
 REF. REVISTA/LIBRO: 2nd European Distributed Memory Computing Conference, Munich-Germany. Lecture Notes in Computer Science, Springer-Verlag . Vol. 487, pp. 173-182. April 1991
 También en 2^{as} Jornadas de Paralelismo, El Escorial-Madrid (Spain), 1991.
 CLAVE: A
- AUTORES: J. Labarta, E. Ayguadé, J. Torres, J.M. Llaberia and M. Valero
 TITULO: Balanced Loop Partitioning Using GTS
 REF. REVISTA/LIBRO: Fourth Workshop on Languages and Compilers for Parallel Computing, Santa Clara (CA), August 1991.
 También en Languages and Compilers for Parallel Computing, U. Banerjee et al. (Eds.), chapter 19 (pp. 298-312), Lecture Notes in Computer Science, Springer-Verlag. Vol. 589. 1992
 ISSN/ISBN: 354055422
 CLAVE: CL
- AUTORES: J. Cortadella, R. Badia and E. Ayguadé
 TITULO: Scheduling in a Continuous Area-Time Design Space
 REF. REVISTA/LIBRO: 17th EUROMICRO Conference. Microprocessing and Microprogramming, North Holland. Vol. 32, no. 1-5, pp. 199-206. September 1991
 CLAVE: A
- AUTORES: M. Valero, T. Lang, J.M. Llaberia, M. Peiron, J.J. Navarro and E. Ayguadé
 TITULO: Conflict-Free Strides for Vectors in Matched Memories
 REF. REVISTA/LIBRO: Parallel Processing Letters, World Scientific Publishing Co. Vol.1, no. 2, pp. 95-102. 1991
 ISSN/ISBN: 0129-6264
 CLAVE: A
- AUTORES: M. Valero, T. Lang, J.M. Llaberia, M. Peiron, E. Ayguadé and J.J. Navarro
 TITULO: Increasing the Number of Strides for Conflict-Free Vector Access
 REF. REVISTA/LIBRO: ISCA'92, International Symposium on Computer Architecture, Gold Coast (Australia). ACM Computer Architecture News. Vol. 20, no. 2, pp. 372-381. May 1992
 ISSN/ISBN: 0163-5964
 CLAVE: A
- AUTORES: M. Valero, T. Lang and E. Ayguadé
 TITULO: Conflict-Free Access of Vectors with Power-of-Two Strides
 REF. REVISTA/LIBRO: 6th ACM International Conference on Supercomputing ICS, Washington. pp. 149-156. July 1992
 CLAVE: Conf
- AUTORES: J. Torres, E. Ayguadé, J. Labarta, M. Valero and J.M. Llaberia
 TITULO: Partitioning the Iteration Space for Distributed-Memory Multiprocessors
 REF. REVISTA/LIBRO: Int. Conference on Parallel Computing and Transputer Applications, IOS Press, Barcelona. pp. 683-692. September 1992
 ISSN/ISBN: 9051990960
 CLAVE: Conf

- AUTORES: J. Llosa, E. Ayguadé and J. Labarta
 TITULO: Design of a Shared-Memory Cluster for a Distributed-Memory Multiprocessor
 REF. REVISTA/LIBRO: Int. Conference on Parallel Computing and Transputer Applications, Barcelona. pp. 526-535. September 1992
 CLAVE: Conf
- AUTORES: M. Valero, M. Peiron and E. Ayguadé
 TITULO: Access to Streams in Multiprocessor Systems
 REF. REVISTA/LIBRO: EUROMICRO Workshop on Parallel and Distributed Processing, Gran Canaria. pp. 310-316. January 1993
 CLAVE: Conf
- AUTORES: J. Torres, E. Ayguadé, J. Labarta and J. Llosa
 TITULO: Parallel Execution of Loops with Conditional Statements
 REF. REVISTA/LIBRO: 11th International Conference Applied Informatics, Annecy, France. pp. 37-40. May 1993
 CLAVE: Conf
- AUTORES: E. Ayguadé and J. Torres
 TITULO: Partitioning the Statement per Iteration Space Using Non-singular Matrices
 REF. REVISTA/LIBRO: 7th ACM International Conference on Supercomputing ICS, Tokyo, Japan. pp. 407-415. July 1993
 CLAVE: Conf
- AUTORES: J. Torres, E. Ayguadé, J. Labarta and M. Valero
 TITULO: Align and Distribute based Linear Loop Transformations
 REF. REVISTA/LIBRO: Sixth Workshop on Languages and Compilers for Parallel Computing, Portland (OR), August 1993.
 Tambien en Languages and Compilers for Parallel Computing, U. Banerjee et al. (Eds.), Lecture Notes in Computer Science, Springer-Verlag . Vol. 768, pp. 321-339. 1994
 ISSN/ISBN: 3-540-57659-2
 CLAVE: A
- AUTORES: M. Peiron, M. Valero, E. Ayguadé and T. Lang
 TITULO: Conflict-Free Access to Streams in Multiprocessor Systems
 REF. REVISTA/LIBRO: 19th EUROMICRO Conference, Barcelona. Microprocessing and Microprogramming. Vol. 38, no. 1-5, pp. 119-130. September 1993
 ISSN/ISBN: 0165-6074
 CLAVE: A
- AUTORES: M. Peiron, M. Valero, E. Ayguadé and T. Lang
 TITULO: Synchronized Access to Streams in Vector Multiprocessors
 REF. REVISTA/LIBRO: IEEE TC on Computer Architecture Newsletter. Fall issue, pp. 37-41. 1993
 CLAVE: A
- AUTORES: J. Torres, E. Ayguadé, J. Labarta, J.M. Llaberia and M. Valero
 TITULO: Automatic Data-Mapping for Distributed-Memory Multiprocessor Systems
 REF. REVISTA/LIBRO: International Journal of Mini and Microcomputers. Vol. 15, no. 3, pp. 109-115. 1993
 ISSN/ISBN: 0702-0481
 CLAVE: A
- AUTORES: E. Ayguadé, J. Labarta, J. Garcia, M. Gironès and M. Valero
 TITULO: A Study of Data Sets and Affinity in the Perfect Club™
 REF. REVISTA/LIBRO: 4th International Workshop on Compilers for Parallel Computers, Delft (The Netherlands). pp. 5-16. December 1993
 CLAVE: Conf

- AUTORES: M. Valero, M. Peiron and E. Ayguadé
TITULO: Access to Vectors in Multi-module Memories
REF. REVISTA/LIBRO: 2nd EUROMICRO Workshop on Parallel and Distributed Processing, Malaga. pp. 228-236. January 1994
CLAVE: Conf
- AUTORES: J. Torres and E. Ayguadé
TITULO: Compilació per a Supercomputadors
REF. REVISTA/LIBRO: Butlletí de les Societats Catalanes de Física, Química, Matemàtiques i Tecnologia, Institut d'Estudis Catalans. Vol. XIV, no. 2, pp. 235-246. Juny 1994
ISSN/ISBN: 1130-4758
CLAVE: A
- AUTORES: M. Peiron M. Valero, and E. Ayguadé
TITULO: Synchronized Access to Streams in SIMD Vector Multiprocessors
REF. REVISTA/LIBRO: 8th ACM International Conference on Supercomputing ICS, Manchester (UK). pp. 23-32. July 1994
CLAVE: Conf
- AUTORES: E. Ayguadé, J. Garcia, M. Girones, J. Labarta, J. Torres and M. Valero
TITULO: Detecting and Using Affinity in an Automatic Data Distribution Tool
REF. REVISTA/LIBRO: 7th Workshop on Languages and Compilers for Parallel Computing, Ithaca (NY), August 1994.
Tambien en Languages and Compilers for Parallel Computing, (Springer-Verlag), K. Pingali et al. (Eds.), Lecture Notes in Computer Science, Springer-Verlag. Vol. 892. pp. 61-75. 1995
CLAVE: A
- AUTORES: M. Valero, M. Peiron and E. Ayguadé
TITULO: Memory Access Synchronization in Vector Multiprocessors
REF. REVISTA/LIBRO: CONPAR 94 - VAPP VI, Linz (Austria). Lecture Notes in Computer Science, Springer-Verlag. Vol. 854, pp. 414-425. September 1994
CLAVE: A
- AUTORES: J. Llosa, M. Valero, J. Fortes and E. Ayguadé
TITULO: Using Sacks to Organize Registers in VLIW Machines
REF. REVISTA/LIBRO: CONPAR 94 - VAPP VI, Linz (Austria). Lecture Notes in Computer Science, Springer-Verlag. Vol. 854, pp. 628-639. September 1994
CLAVE: A
- AUTORES: E. Ayguadé, J. Labarta, J. Garcia, M. Gironès and M. Valero
TITULO: Detecting Affinity for Automatic Data Distribution
REF. REVISTA/LIBRO: 2nd International Workshop on Massive Parallelism, Capri-Italy, pp. 32-46. October 1994
CLAVE: Conf
- AUTORES: J. Llosa, M. Valero, E. Ayguadé, and J. Labarta
TITULO: Register Requirements of Software Pipelined Loops and its Effects on Performance
REF. REVISTA/LIBRO: 2nd International Workshop on Massive Parallelism, Capri-Italy, pp. 173-189. October 1994
CLAVE: Conf
- AUTORES: C. Barrado, J. Labarta and E. Ayguadé
TITULO: An Efficient Scheduling for DOACROSS Loops
REF. REVISTA/LIBRO: Parallel and Distributed Computing and Systems, Washington DC. pp. 303-307. October 1994
CLAVE: Conf

- AUTORES: M. Valero, E. Ayguadé, and M. Peiron
 TITULO: Network Synchronization and Out-of-order Access to Vectors
 REF. REVISTA/LIBRO: Parallel Processing Letters, World Scientific Publishing Co. Vol.4, no. 4, pp. 405-415. 1994
 ISSN/ISBN: 0129-6264
 CLAVE: A
- AUTORES: R. Espasa, M. Valero, D. Padua, M. Jiménez and E. Ayguadé
 TITULO: Quantitative Analysis of Vector Code
 REF. REVISTA/LIBRO: 3rd EUROMICRO Workshop on Parallel and Distributed Processing, San Remo (Italia). pp. 452-461. January 1995
 CLAVE: Conf
- AUTORES: J. Llosa, M. Valero and E. Ayguadé
 TITULO: Non-consistent Dual Register Files to Reduce Register Pressure
 REF. REVISTA/LIBRO: IEEE Int. Symp. on High-Performance Computer Architecture HPCA-1, Raleigh (USA), pp. 22-31. January 1995
 CLAVE: Conf
- AUTORES: J. Garcia, E. Ayguadé and J. Labarta
 TITULO: A Novel Approach Towards Automatic Data Distribution
 REF. REVISTA/LIBRO: 2nd Workshop on Automatic Data Layout and Performance Prediction, 18-21/4/1995, Rice University. CRPC Technical Report CRPC-TR95548, Houston-TX (USA), April 1995
 CLAVE: Conf
- AUTORES: M. Valero, T. Lang, M. Peiron and E. Ayguadé
 TITULO: Conflict-Free Access for Streams in Multi-module Memories
 REF. REVISTA/LIBRO: IEEE Transactions on Computers. Vol. 44, no. 5, pp. 634-646. May 1995
 ISSN/ISBN: 0018-9340
 CLAVE: A
- AUTORES: M. Peiron, M. Valero, E. Ayguadé and T. Lang
 TITULO: Vector Multiprocessors with Arbitrated Memory Access
 REF. REVISTA/LIBRO: 22nd Int. Symposium on Computer Architecture, S. Margherita Ligure (Italy). ACM Computer Architecture News. Vol. 23, no. 2, pp. 243-252. June 1995
 CLAVE: A
- AUTORES: C. Barrado, J. Labarta, E. Ayguadé and M. Valero
 TITULO: Automatic Generation of Loop Scheduling for VLIW
 REF. REVISTA/LIBRO: 3rd Parallel Architectures and Compilation Techniques PACT'95, Cyprus (Greece). pp. 306-309. June 1995
 CLAVE: Conf
- AUTORES: J. Torres, E. Ayguadé, J. Labarta, and M. Valero
 TITULO: Revisiting Framework of Linear Loop Transformations
 REF. REVISTA/LIBRO: 5th International Workshop on Compilers for Parallel Computers, Malaga. pp. 187-199. June 1995
 CLAVE: Conf
- AUTORES: C. Barrado, J. Labarta, E. Ayguadé, and M. Valero
 TITULO: Generating a Periodic Pattern for VLIW
 REF. REVISTA/LIBRO: 5th International Workshop on Compilers for Parallel Computers, Malaga. pp. 485-502. June 1995
 CLAVE: Conf

- AUTORES: J. Llosa, M. Valero, and E. Ayguadé
 TITULO: Bidirectional Scheduling to Minimize Register Requirements
 REF. REVISTA/LIBRO: 5th International Workshop on Compilers for Parallel Computers, Malaga.
 pp. 534-554. June 1995
 CLAVE: Conf
- AUTORES: E. Ayguadé, C. Barrado, J. Labarta, J. Llosa, D. Lopez, S. Moreno, D. Padua, E. Riera
 and M. Valero
 TITULO: ICTINEO: Una Herramienta para la Investigación en Paralelismo a Nivel de Instrucciones
 REF. REVISTA/LIBRO: 6as Jornadas de Paralelismo, Barcelona (Spain). Julio 1995
 CLAVE: Conf
- AUTORES: E. Ayguadé, J. Garcia, M. Girones, M.L. Grande and J. Labarta
 TITULO: Data Redistribution in an Automatic Data Distribution Tool
 REF. REVISTA/LIBRO: 8th Workshop on Languages and Compilers for Parallel Computing, San
 Jose (CA), August 1995.
 Tambien en Languages and Compilers for Parallel Computing, (Springer-Verlag), Huang et al. (Eds.)
 Lecture Notes in Computer Science, Springer-Verlag. Vol. 1033, pp. 271-287. 1996
 CLAVE: A
- AUTORES: E. Ayguadé, P. Knijnenburg, and J. Torres,
 TITULO: Multi-transformations: Definition and Usefulness
 REF. REVISTA/LIBRO: XV Int. Conference of the Chilean Computer Science Society, Arica-Chile.
 pp. 37-47. October 1995
 CLAVE: Conf
- AUTORES: J. Garcia, E. Ayguadé and J. Labarta,
 TITULO: A Novel Approach Towards Automatic Static Data Distribution
 REF. REVISTA/LIBRO: IEEE/ACM Supercomputing'95, Pp. 78-78, San Diego-CA (USA).
 December 1995
 CLAVE: Conf
- AUTORES: J. Llosa, M. Valero, E. Ayguadé and A. Gonzalez,
 TITULO: Hypernode Reduction Modulo Scheduling
 REF. REVISTA/LIBRO: MICRO-28, 28th International Symposium on Microarchitecture, Ann
 Arbor-MI (USA). pp. 350-360. December 1995
 CLAVE: Conf
- AUTORES: E. Ayguadé, J. Labarta, J. Garcia, M. Gironès and M. Valero,
 TITULO: Analyzing Reference Patterns in Automatic Data Distribution Tools
 REF. REVISTA/LIBRO: International Journal of Parallel Programming. Vol. 23, no. 6, pp. 515-535.
 December 1995
 ISSN/ISBN: 0885-7458
 CLAVE: A
- AUTORES: J. Garcia, E. Ayguadé and J. Labarta,
 TITULO: Using a 0-1 Integer Programming Model for Automatic Static Data Distribution
 REF. REVISTA/LIBRO: Parallel Processing Letters, World Scientific Publishing Co. Vol. 6, no. 1,
 pp. 159-171. 1996
 ISSN/ISBN: 0129-6264
 CLAVE: A
- AUTORES: J. Torres, E. Ayguadé, J. Labarta, and M. Valero
 TITULO: Loop Parallelization: Revisiting Framework of Linear Loop Transformations
 REF. REVISTA/LIBRO: 4th EUROMICRO Workshop on Parallel and Distributed Processing,
 Braga (Portugal). pp. 420-427. January 1996
 CLAVE: Conf

- AUTORES: X. Martorell, J. Labarta, J.I. Navarro and E. Ayguadé
 TITULO: A Library Implementation of the Nano-Threads Programming Model
 REF. REVISTA/LIBRO: Euro-Par'96, Lyon (France). Lecture Notes in Computer Science, Springer-Verlag. Vol. 1124, pp. 644-649. August 1996
 CLAVE: A
- AUTORES: E. Ayguadé, J. Garcia, M. L. Grande, and J. Labarta,
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CLAVE: A
- AUTORES: Xavier Teruel, Priya Unnikrishnan, Xavier Martorell, Eduard Ayguadé, Raul Silvera, Guansong Zhang and Ettore Tiotto
TITULO: OpenMP Tasks in IBM XL compilers
REVISTA/LIBRO: 18th Annual International Centre for Advanced Studies Research Conference (CASCON 2008), Toronto (Canada). October 27-30, 2008.
CLAVE: Conf
- AUTORES: Marc Gonzalez, Nikola Vujic, Xavier Martorell, Eduard Ayguadé, Alexandre E. Eichenberger, Tong Chen, Zehra Sura, Tao Zhang, Kevin O'Brien, and Kathryn O'Brien
TITULO: Hybrid Access-Specific Software Cache Techniques for the Cell BE Architecture
REF. REVISTA/LIBRO: The 17th International Conference on Parallel Architectures and Compilation Techniques (PACT). Toronto, Canada. October 25-29, 2008.
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.88 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Ferad Zyulkyarov, Sanja Cvijic, Osman Unsal, Adrian Cristal, Eduard Ayguadé, Tim Harris and Mateo Valero
TITULO: WormBench - A Configurable Workload for Evaluating Transactional Memory Systems
REF. REVISTA/LIBRO: MEDEA -- Memory performance: DEaling with Applications, systems and architecture (workshop held in conjunction with PACT-2008). Toronto, Canada. October 25-29, 2008.
CLAVE: Conf
- AUTORES: Roger Ferrer, Marc Gonzalez, Federico Silla, Xavier Martorell, Eduard Ayguadé
TITULO: Evaluation of Memory Performance on the Cell BE with the SARC Programming Model
REF. REVISTA/LIBRO: MEDEA -- MEMory performance: DEaling with Applications, systems and architecture (workshop held in conjunction with PACT-2008). Toronto, Canada. October 25-29, 2008.
CLAVE: Conf
- AUTORES: Barbara M. Chapman, Weimin Zheng, Guang R. Gao, Mitsuhsa Sato, Eduard Ayguadé and Dongsheng Wang (Eds.)
TITULO: A Practical Programming Model for the Multi-Core Era
REF. REVISTA/LIBRO: 3rd International Workshop on OpenMP, IWOMP 2007, Beijing, China, June 3-7, Lecture Notes in Computer Science 4935, ISBN 978-3-540-69302-4. Springer 2008.
CLAVE: L
- AUTORES: D. Carrera, V. Beltran, J. Torres and E. Ayguadé
TITULO: A Hybrid Connector for Efficient Web Servers
REF. REVISTA/LIBRO: International Journal on High Performance Computing and Networking (IJHPCN), InderScience Publishers, Vol. 5, Nos. 5/6, pp.323-330. ISSN: 1740-0562. 2008.
CLAVE: A
- AUTORES: Alex Duran, Julita Corbalan and Eduard Ayguade
TITULO: An adaptive cut-off for task parallelism
REVISTA/LIBRO: SC '08: Proceedings of the 2008 ACM/IEEE conference on Supercomputing. Austin, TX (USA). November 2008
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.96 in CS conference ranking (2008)
CLAVE: Conf

- AUTORES: Vicenç Beltran, Jordi Torres and Eduard Ayguadé
TITULO: Improving Web Server Performance Through Main Memory Compression
REVISTA/LIBRO: The 14th IEEE International Conference on Parallel and Distributed Systems (ICPADS'08). Melbourne (Australia). December 8-10, 2008
CLAVE: Conf
- AUTORES: David Carrera, Malgorzata Steinder, Ian Whalley, Jordi Torres and Eduard Ayguadé
TITULO: Enabling Resource Sharing between Transactional and Batch Workloads Using Dynamic Application Placement
REVISTA/LIBRO: 9th ACM/IFIP/USENIX International Middleware Conference (Middleware 2008). Leuven (Belgium). December 1-5, 2008. In Lecture Notes in Computer Science, vol. 5346/2008, pp. 203-222. ISSN: 0302-9743. December 2008.
CLAVE: Conf
- AUTORES: Eduard Ayguadé, Nawal Copty, Alejandro Duran, Jay Hoeflinger, Yuan Lin, Federico Massaioli, Xavier Teruel, Priya Unnikrishnan and Guansong Zhang
TITULO: The Design of OpenMP Tasks
REVISTA/LIBRO: IEEE Transactions on Parallel and Distributed Systems, vol. 20, no. 3, pp. 404-418, January 2009
Impact factor: 1.733 (Rank 20/92-Q1 in COMPUTER SCIENCE, THEORY & METHODS and 62/246-Q2 in ENGINEERING, ELECTRICAL & ELECTRONIC)
CLAVE: A
- AUTORES: Yolanda Becerra, David Carrera and Eduard Ayguadé
TITULO: Batch Job Profiling and Adaptive Profile Enforcement for Virtualized Environments
REVISTA/LIBRO: The 17th Euromicro International Conference on Parallel, Distributed, and Network-Based Processing, 18-20 February 2009, Weimar (Germany).
CLAVE: Conf
- AUTORES: Rosa M. Badia, Josep M. Perez, Eduard Ayguadé and Jesus Labarta
TITULO: Impact of the memory hierarchy on shared memory architectures in multicore programming models
REVISTA/LIBRO: The 17th Euromicro International Conference on Parallel, Distributed, and Network-Based Processing, 18-20 February 2009, Weimar (Germany).
CLAVE: Conf
- AUTORES: Ferad Zulkaryov, Vladimir Gajinov, Osman Unsal, Adrian Cristal, Eduard Ayguadé, Tim Harris, Mateo Valero
TITULO: Atomic Quake: Using Transactional Memory in an Interactive Multiplayer Game Server
REVISTA/LIBRO: 14th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP). February 14-18, 2009, Raleigh, North Carolina (USA).
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.81 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Chinmay Kulkarni, Osman Unsal, Adrian Cristal, Eduard Ayguadé, Mateo Valero
TITULO: Turbocharging boosted transactions or: How I Learnt to Stop Worrying and Love Longer Transactions
REVISTA/LIBRO: 14th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP). February 14-18, 2009, Raleigh, North Carolina (USA).
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.81 in CS conference ranking (2008)
CLAVE: Conf

- AUTORES: Eduard Ayguadé, Rosa M. Badia, Daniel Cabrera, Alejandro Duran, Marc Gonzalez, Francisco Igual, Daniel Jimenez, Jesus Labarta, Xavier Martorell, Rafael Mayo, Josep M. Perez and Enrique S. Quintana-Orti
TITULO: A Proposal to Extend the OpenMP Tasking Model for Heterogeneous Architectures
REVISTA/LIBRO: 5th International Workshop on OpenMP (IWOMP-2009). June 3-5, 2009. Dresden (Germany). Evolving OpenMP in an Age of Extreme Parallelism, Lecture Notes in Computer Science, vol. 5568, Springer, 2009
CLAVE: Conf
- AUTORES: V. Gajinov, F. Zyulkyarov, A. Cristal, O. Unsal, E. Ayguadé, T. Harris, and M. Valero
TITULO: QuakeTM: Parallelizing a Complex Serial Application Using Transactional Memory
REVISTA/LIBRO: 23rd International Conference on Supercomputing (ICS'09), pp. 126-135. June 8-12, 2009. Yorktown Heights, NY, USA
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.74 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Montse Ferreras, Vladimir Marjanovic, Eduard Ayguadé and Jesus Labarta
TITULO: Gaining asynchrony by using hybrid UPC/SMPs
REVISTA/LIBRO: Workshop on Asynchrony in the PGAS Programming Model (APGAS, collocated with ICS 2009). June 8, 2009. Yorktown Heights, NY, USA
CLAVE: Conf
- AUTORES: Vicenç Beltran, Xavier Martorell, Jordi Torres and Eduard Ayguadé
TITULO: Accelerating Software Memory Compression on the Cell/B.E.
REVISTA/LIBRO: Workshop on Hardware Accelerators for High Performance Computing (WAHA, collocated with ICS 2009). June 8, 2009. Yorktown Heights, NY, USA
CLAVE: Conf
- AUTORES: Alejandro Duran, Roger Ferrer, Eduard Ayguadé, Rosa M. Badia and Jesús Labarta
TITULO: A Proposal to Extend the OpenMP Tasking Model with Dependent Tasks
REVISTA/LIBRO: International Journal of Parallel Programming, vol. 37, no. 3, pp. 292-305. June 2009.
Impact factor: 0.816 (Rank 61/92-Q3 in COMPUTER SCIENCE, THEORY & METHODS)
CLAVE: A
- AUTORES: Rudolf Eigenmann and Eduard Ayguadé
TITULO: Guest Editor's Introduction (Special issue on IWOMP 2008)
REVISTA/LIBRO: International Journal of Parallel Programming, vol. 37, no. 3, pp. 247-249. June 2009.
Impact factor: 0.816 (Rank 61/92-Q3 in COMPUTER SCIENCE, THEORY & METHODS)
CLAVE: A
- AUTORES: Daniel Cabrera, Xavier Martorell, Georgi Gaydadjiev, Eduard Ayguadé and Daniel Jiménez-González
TITULO: OpenMP extensions for FPGA Accelerators
REVISTA/LIBRO: IC-SAMOS - International Conference on Embedded Computer Systems: Architectures, Modeling, and Simulation. Samos, Greece, July 20-23, 2009.
CLAVE: Conf
- AUTORES: Paul Carpenter, Alex Ramirez and Eduard Ayguadé
TITULO: The Abstract Streaming Machine: Compile-time performance modelling of stream programs on heterogeneous multiprocessors
REVISTA/LIBRO: SAMOS Workshop - International Workshop on Systems, Architectures, Modeling, and Simulation. Samos, Greece, July 20-23, 2009 (Best paper award).
CLAVE: Conf

- AUTORES: Judit Planas, Rosa M. Badia, Eduard Ayguadé and Jesus Labarta
TITULO: Hierarchical task based programming with StarSs
REVISTA/LIBRO: International Journal of High Performance Computing Applications. Vol. 23, no. 3, pp. 284-299. July 2009.
Impact factor: 1.274 (Rank 21/49-Q2 in COMPUTER SCIENCE, HARDWARE & ARCHITECTURE, 34/92-Q2 in COMPUTER SCIENCE, THEORY & METHODS and 47/95-Q2 in COMPUTER SCIENCE, INTERDISCIPLINARY APPLICATIONS)
CLAVE: A
- AUTORES: Eduard Ayguadé, Rosa M. Badia, Francisco Igual-Peña, Jesús Labarta, Rafael Mayo, Enrique Quintana-Orti
TITULO: An Extension of the StarSs Programming Model for Platforms with Multiple GPUs
REVISTA/LIBRO: Euro-Par 2009, Delft (The Netherlands), August 25-28, 2009.
CLAVE: Conf
- AUTORES: Roger Ferrer, Vicenç Beltran, Marc Gonzalez, Xavier Martorell and Eduard Ayguadé
TITULO: Achieving High Memory Performance from heterogeneous architectures with the SARC Programming Model
REF. REVISTA/LIBRO: MEDEA 2009 -- MEMory performance: DEaling with Applications, systems and architecture (workshop held in conjunction with PACT-2009). Raleigh, North Carolina. Sept. 12-16 2009.
CLAVE: Conf
- AUTORES: Alejandro Duran, Xavier Teruel, Roger Ferrer, Xavier Martorell and Eduard Ayguadé
TITULO: Barcelona OpenMP Tasks Suite: A Set of Benchmarks Targeting the Exploitation of Task Parallelism in OpenMP
REVISTA/LIBRO: 38th International Conference on Parallel Processing (ICPP-2009), Vienna (Austria), September 22-25, 2009.
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.61 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Yolanda Becerra, Vicenç Beltran, David Carrera, Marc González, Jordi Torres and Eduard Ayguadé
TITULO: Speeding Up Distributed MapReduce Applications Using Hardware Accelerators
REVISTA/LIBRO: 38th International Conference on Parallel Processing (ICPP-2009), Vienna (Austria), September 22-25, 2009.
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.61 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Nikola Vujic, Lluc Alvarez, Marc Gonzalez, Xavier Martorell and Eduard Ayguadé
TITULO: Adaptive and Speculative Memory Consistency Support for Multi-core Architectures with Local Memories
REVISTA/LIBRO: 22nd International Workshop on Languages and Compilers for Parallel Computing, University of Delaware, Newark, Delaware, USA. October 8-10, 2009.
CLAVE: Conf
- AUTORES: Roger Ferrer, Alejandro Duran, Xavier Martorell and Eduard Ayguadé
TITULO: Unrolling Loops Containing Task Parallelism
REVISTA/LIBRO: 22nd International Workshop on Languages and Compilers for Parallel Computing, University of Delaware, Newark, Delaware, USA. October 8-10, 2009.
CLAVE: Conf

- AUTORES: Paul Carpenter, Alex Ramirez and Eduard Ayguadé
TITULO: Mapping Stream Programs onto Heterogeneous Multiprocessor Systems
REVISTA/LIBRO: Proceedings of CASES 2009: International Conference on Compilers, Architecture, and Synthesis for Embedded Systems, pp. 57-66. October 11-16, 2009. Grenoble (France).
Calidad del congreso: Tipo A en CORE 2008 Conference ranking and EIC=0.81 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Jesús Labarta, Eduard Ayguadé and Mateo Valero
TITULO: BSC Vision Towards Exascale
REVISTA/LIBRO: International Journal of High Performance Computing Applications. Vol. 23, no. 4, pp. 340-343. October 2009.
Impact factor: 1.274 (Rank 21/49-Q2 in COMPUTER SCIENCE, HARDWARE & ARCHITECTURE, 34/92-Q2 in COMPUTER SCIENCE, THEORY & METHODS and 47/95-Q2 in COMPUTER SCIENCE, INTERDISCIPLINARY APPLICATIONS)
CLAVE: A
- AUTORES: Xavier Teruel, Christopher Barton, Alejandro Duran, Xavier Martorell, Eduard Ayguadé, Priya Unnikrishnan, Guansong Zhang and Raul Silvera
TITULO: OpenMP Tasking Analysis for Programmers
REVISTA/LIBRO: 19th Annual International Centre for Advanced Studies Research Conference (CASCON 2009). November 2-5, 2009. Markham, ON (USA).
CLAVE: Conf
- AUTORES: Muhammad Shafiq, Miquel Pericàs, Raul de la Cruz, Mauricio Araya-Polo, Nacho Navarro and Eduard Ayguadé
TITULO: Exploiting Memory Customization in FPGA for 3D Stencil Computations
REVISTA/LIBRO: The 2009 International Conference on Field-Programmable Technology (FPT'09). The University of New South Wales. Sydney, Australia. 9-11 December 2009
CLAVE: Conf
- AUTORES: Vicenç Beltran, David Carrera, Jordi Torres and Eduard Ayguadé
TITULO: CellMT: A Cooperative Multithreading Library for the Cell/B.E.
REVISTA/LIBRO: 16th Annual International Conference on High Performance Computing (HiPC-2009). December 16-19, 2009. Kochi, Cochin (India).
Calidad del congreso: Tipo A en CORE 2010 Conference ranking
CLAVE: Conf
- AUTORES: Nikola Vujic, Felipe Cabarcas, Marc Gonzalez, Alex Ramirez, Xavier Martorell and Eduard Ayguadé
TITULO: DMA++: On the Fly Data Realignment for On-Chip Memories
REVISTA/LIBRO: The 16th IEEE International Symposium on High-Performance Computer Architecture (HPCA-16). Bangalore, India. January 9-14, 2010.
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.96 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Vladimir Marjanovic, Jesús Labarta, Eduard Ayguadé, and Mateo Valero
TITULO: Effective Communication and Computation Overlap with Hybrid MPI/SMPs
REVISTA/LIBRO: 15th ACM SIGPLAN Annual Symposium on Principles and Practice of Parallel Programming (PPoPP). Bangalore, India. January 9-14, 2010.
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.81 in CS conference ranking (2008)
CLAVE: Conf

- AUTORES: Roger Ferrer, Vicenç Beltran, Marc González, Xavier Martorell and Eduard Ayguadé
TITULO: Analysis of Task Offloading for Accelerators
REVISTA/LIBRO: HiPEAC 2010 -- International conference on High-Performance Embedded Architectures and Compilers. Pisa, Italy. January 25-27, 2010.
CLAVE: Conf
- AUTORES: Paul Carpenter, Alex Ramirez and Eduard Ayguadé
TITULO: Buffer sizing for self-timed stream programs on heterogeneous distributed memory multiprocessors
REVISTA/LIBRO: HiPEAC 2010 -- International conference on High-Performance Embedded Architectures and Compilers. Pisa, Italy. January 25-27, 2010.
CLAVE: Conf
- AUTORES: Bramimir Dickov, Miquel Pericàs, Nacho Navarro and Eduard Ayguadé
TITULO: Row-interleaved Streaming Data Flow Implementation of Sparse Matrix Vector Multiplication in FPGA
REVISTA/LIBRO: 4th HiPEAC Workshop on Reconfigurable Computing (WRC10), in conjunction with HiPEAC 2010 -- International conference on High-Performance Embedded Architectures and Compilers. Pisa, Italy. January 23, 2010.
CLAVE: Conf
- AUTORES: Jordi Guitart, Jordi Torres and Eduard Ayguadé
TITULO: A Survey on Performance Management for Internet Applications
REVISTA/LIBRO: Concurrency and Computation: Practice and Experience, Vol. 22 (1), pp. 68-106, January 2010
Impact factor: 1.004 (Rank 58/93-Q3 in COMPUTER SCIENCE, SOFTWARE ENGINEERING and 51/92-Q3 in COMPUTER SCIENCE, THEORY & METHODS)
CLAVE: A
- AUTORES: Ramon Bertran, Marc Gonzalez, Xavier Martorell, Nacho Navarro and Eduard Ayguadé
TITULO: Local Memory Design Space Exploration for High Performance Computing
REVISTA/LIBRO: The Computer Journal, Oxford University Press. DOI: 10.1093/comjnl/bxq026. March 2010
Impact factor: 1.394 (Rank 17/49-Q2 in COMPUTER SCIENCE, HARDWARE & ARCHITECTURE, 50/116-Q2 in COMPUTER SCIENCE, INFORMATION SYSTEMS and 36/93-Q2 in COMPUTER SCIENCE, SOFTWARE ENGINEERING)
CLAVE: A
- AUTORES: Jordà Polo, David Carrera, Yolanda Becerra, Malgorzata Steinder, Ian Whalley, Jordi Torres and Eduard Ayguadé
TITULO: Performance-Driven Task Co-Scheduling for MapReduce Environments
REVISTA/LIBRO: NOMS-2010 -- 12th IEEE/IFIP Network Operations and Management Symposium, Osaka, Japan. April 19-23, 2010.
CLAVE: Conf
- AUTORES: Nikola Vujic, Marc Gonzalez, Xavier Martorell and Eduard Ayguadé
TITULO: Automatic Prefetch and Modulo Scheduling Transformations for the Cell BE Architecture
REVISTA/LIBRO: IEEE Transactions on Parallel and Distributed Systems, vol. 21, no. 4, pp. 494-505. April 2010.
Impact factor: 1.733 (Rank 20/92-Q1 in COMPUTER SCIENCE, THEORY & METHODS and 62/246-Q2 in ENGINEERING, ELECTRICAL & ELECTRONIC)
CLAVE: A
- AUTORES: Jordi Torres, Eduard Ayguadé, David Carrera, Jordi Guitart, Vicenç Beltran, Yolanda Becerra, Rosa M. Badia, Jesús Labarta and Mateo Valero
TÍTULO: BSC contributions in Energy-aware Resource Management for Large Scale Distributed Systems
REVISTA/LIBRO: Proceedings of the 1st COST Action IC0804 on Large Scale Distributed Systems. Jean-Marc Pierson, Helmut Hlavacs (Ed.) pp. 76-79. ISBN: 978-2-917490-10-5.
CLAVE: Conf

- AUTORES: Vladimir Marjanovic, Jesús Labarta, Eduard Ayguadé and Mateo Valero
 TITULO: Overlapping Communication and Computation by Using a Hybrid MPI/SMPs Approach
 REVISTA/LIBRO: 24th ACM International Conference on Supercomputing (ICS'10), Tsukuba, Japan. June 1-4, 2010.
 Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.74 in CS conference ranking (2008)
 CLAVE: Conf
- AUTORES: Ramon Bertran, Marc Gonzalez, Xavier Martorell, Nacho Navarro and Eduard Ayguadé
 TITULO: Decomposable and Responsive Power Models for Multicore Processors using Performance Counters
 REVISTA/LIBRO: 24th ACM International Conference on Supercomputing (ICS'10), Tsukuba, Japan. June 1-4, 2010.
 Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.74 in CS conference ranking (2008)
 CLAVE: Conf
- AUTORES: Eduard Ayguadé, James Beyer, Alejandro Duran, Roger Ferrer, Grant Haab, Kelvin Li and Federico Massaioli
 TITULO: An Extension to Improve Tasking Control
 REVISTA/LIBRO: IWOMP 2010 – 6th International Workshop on OpenMP, Tsukuba, Japan. June 14-16, 2010.
 CLAVE: Conf
- AUTORES: Alejandro Duran, Roger Ferrer, Michael Klemm, Bronis R. de Supinski and Eduard Ayguadé
 TITULO: A Proposal for User-defined Reductions in OpenMP
 REVISTA/LIBRO: IWOMP 2010 – 6th International Workshop on OpenMP, Tsukuba, Japan. June 14-16, 2010.
 CLAVE: Conf
- AUTORES: Yoav Etsion, Alex Ramirez, Rosa M. Badia, Eduard Ayguade, Jesus Labarta, Mateo Valero
 TITULO: Task Superscalar: Using Processors as Functional Units
 REVISTA/LIBRO: HotPar'10 – 2nd USENIX Workshop on Hot Topics in Parallelism, Berkeley (CA), USA. June 14-15, 2010.
 CLAVE: Conf
- AUTORES: Roger Ferrer, Alex Duran, Xavier Martorell and Eduard Ayguadé
 TÍTULO: Harmonizing Serial Optimizations with OpenMP
 REVISTA/LIBRO: 15th Workshop on Compilers for Parallel Computing (CPC-2010). Vienna (Austria). July 7-9, 2010.
 CLAVE: Conf
- AUTORES: Eduard Ayguadé, Rosa M. Badia, Pieter Bellens, Daniel Cabrera, Alejandro Duran, Roger Ferrer, Marc González, Francisco Igual, Daniel Jiménez-González, Jesús Labarta, Luis Martinell, Xavier Martorell, Rafael Mayo, Josep M. Pérez, Judit Planas and Enrique Quintana-Ortí
 TÍTULO: Extending OpenMP to Survive the Heterogeneous Multi-Core Era
 REVISTA/LIBRO: International Journal of Parallel Programming, vol. 38, no. 5, pp. 440-459. <<http://dx.doi.org/10.1007/s10766-010-0135-4>>. August 2010.
 Impact factor: 0.816 (Rank 61/92-Q3 in COMPUTER SCIENCE, THEORY & METHODS)
 CLAVE: A
- AUTORES: Matthias Müller and EduardAyguadé,
 TÍTULO: Guest Editors' Introduction
 REVISTA/LIBRO: International Journal of Parallel Programming (Special Issue on OpenMP), vol. 38, no. 5, pp. 339-340. <<http://dx.doi.org/10.1007/s10766-010-0141-6>>. August 2010.
 Impact factor: 0.816 (Rank 61/92-Q3 in COMPUTER SCIENCE, THEORY & METHODS)
 CLAVE: A

- AUTORES: Paul Carpenter, Alex Ramirez and Eduard Ayguadé
TÍTULO: StarSsCheck: A Tool to Find Errors in Task-based Parallel Programs
REVISTA/LIBRO: EuroPar-2010, Ischia (Italy). August 31st – September 3rd, 2010.
CLAVE: Conf
- AUTORES: Muhammad Shafiq, Miquel Pericàs, Nacho Navarro and Eduard Ayguadé
TÍTULO: FEM : A Step Towards a Common Memory Layout for FPGA-based Accelerators
REVISTA/LIBRO: 20th International Conference on Field Programmable Logic and Applications (FPL-2010). Milano (Italy), August 31-September 2nd, 2010.
CLAVE: Conf
- AUTORES: Alessandro Cilardo, Luigi Esposito, Antonio Veniero, Antonino Mazzeo, Vicenç Beltran and Eduard Ayguadé
TÍTULO: A CellBE-based HPC Application for the Analysis of Vulnerabilities in Cryptographic Hash Functions
REVISTA/LIBRO: 12th IEEE International Conference on High Performance and Communications (HPCC'10), Melbourne (Australia). September 1-3, 2010.
CLAVE: Conf
- AUTORES: Daniel Jiménez, Raúl Sánchez, Carlos Álvarez, Julián Morillo, Daniel Cabrera, Xavier Martorell and Eduard Ayguadé
TÍTULO: GPFPGA: entorno para la generación automática de códigos HDL portables entre FPGAs
REVISTA/LIBRO: X Jornadas de Computación Reconfigurable y Aplicaciones (JCRA-2010), Valencia (Spain), September 7-10, 2010.
CLAVE: Conf
- AUTORES: Jordà Polo, David Carrera, Yolanda Becerra, Vicenç Beltran, Jordi Torres and Eduard Ayguadé
TÍTULO: Performance Management of Accelerated MapReduce Workloads in Heterogeneous Clusters
REVISTA/LIBRO: 39th International Conference on Parallel Processing (ICPP-2010), San Diego (USA). September 13-16, 2010.
Calidad del congreso: Tipo A en CORE 2010 Conference ranking and EIC=0.61 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Roger Ferrer, Judit Planas, Pieter Bellens, Alejandro Duran, Marc Gonzalez, Xavier Martorell, Rosa M. Badia, Eduard Ayguadé and Jesus Labarta
TÍTULO: Optimizing the Exploitation of Multicore Processors and GPUs with OpenMP and OpenCL
REVISTA/LIBRO: 23rd International Workshop on Languages and Compilers for Parallel Computing (LCPC2010), Houston, TX (USA), October 7-9, 2010.
CLAVE: Conf
- AUTORES: Ramon Bertran, Yolanda Becerra, David Carrera, Vicenç Beltran, Marc Gonzalez, Xavier Martorell, Jordi Torres and Eduard Ayguadé
TÍTULO: Accurate Energy Accounting for Shared Virtualized Environments using PMC-based Power Modeling Techniques
REVISTA/LIBRO: 11th ACM/IEEE International Conference on Grid Computing (Grid 2010), Brussels (Belgium), October 25-29, 2010.
Calidad del congreso: Tipo A en CORE 2010 Conference ranking
CLAVE: Conf

- AUTORES: Roger Ferrer, Pieter Bellens, Vicenc Beltran, Marc Gonzalez , Xavier Martorell, Rosa M. Badia, Eduard Ayguade, Jae-Seung Yeom, Scott Schneider, Konstantinos Koukos, Michail Alvanos, Dimitros S. Nikolopoulos, Angelos Bilas
TITULO: Parallel Programming Models for Heterogeneous Multicore Architectures
REVISTA/LIBRO: IEEE Micro, vol. 30, no. 5, Special Issue “Multicore: The View from Europe”, pp. 42-53, Sep./Oct. 2010.
Impact factor: 3.205 (Rank 2/49-Q1 in COMPUTER SCIENCE, HARDWARE & ARCHITECTURE and 4/93-Q1 in COMPUTER SCIENCE, SOFTWARE ENGINEERING)
CLAVE: A
- AUTORES: Javier Bueno, Xavier Martorell, Juan José Costa, Toni Cortés, Eduard Ayguadé, Guansong Zhang, Christopher Barton and Raul Silvera
TITULO: Reducing data access latency in SDSM systems using runtime optimizations
REVISTA/LIBRO: 20th Annual International Centre for Advanced Studies Research Conference (CASCON 2010), Toronto (Canada), November 1-4, 2010.
CLAVE: Conf
- AUTORES: Nicolas Poggi, David Carrera, Ricard Gavaldà, Jordi Torres and Eduard Ayguadé
TITULO: Workload and Resource Characterization of an Online Travel and Booking Site
REVISTA/LIBRO: 2010 IEEE International Symposium on Workload Characterization (IISWC-2010). Atlanta, Georgia (USA), December 2–4th, 2010.
CLAVE: Conf
- AUTORES: Yoav Etsion, Felipe Cabarcas, Alejandro Rico, Alex Ramirez, Rosa M. Badia, Eduard Ayguadé, Jesus Labarta and Mateo Valero
TITULO: Task Superscalar: An Out-of-Order Task Pipeline
REVISTA/LIBRO: 43rd International Symposium on Microarchitecture (MICRO-43). Atlanta, Georgia (USA), December 4–8th, 2010.
Calidad del congreso: Tipo A en CORE 2008 Conference ranking and EIC=0.97 in CS conference ranking (2008)
CLAVE: Conf
- AUTORES: Juan Jose Costa, Toni Cortes, Xavier Martorell, Javier Bueno and Eduard Ayguadé
TITULO: Transient Congestion Avoidance in Software DSM Systems
REVISTA/LIBRO: 11th International Conference on Parallel and Distributed Computing, Applications and Technologies (PDCAT). Wuhan (China), December 8–11th, 2010.
CLAVE: Conf
- AUTORES: Eduard Ayguadé and Jordi Torres
TÍTULO: Holistic Management for a more Energy-Efficient Cloud Computing
REVISTA/LIBRO: ERCIM News, Special theme: Cloud Computing. Number 83, October 2010. ISSN 0926-4981
CLAVE: A
- AUTORES: Mauricio Araya-Polo, Javier Cabezas, Mauricio Hanzich, Miquel Pericas, Félix Rubio, Isaac Gelado, Muhammad Shafiq, Enric Morancho, Nacho Navarro, Eduard Ayguade, José María Cela and Mateo Valero
TITULO: Assessing Accelerator-based HPC Reverse Time Migration
REVISTA/LIBRO: IEEE Transactions on Parallel and Distributed Systems, special issue High-Performance Computing with Accelerators, vol. 22, no. 1, pp. 147-162, <<http://doi.ieeecomputersociety.org/10.1109/TPDS.2010.144>>, January 2011.
Impact factor: 1.733 (Rank 20/92-Q1 in COMPUTER SCIENCE, THEORY & METHODS and 62/246-Q2 in ENGINEERING, ELECTRICAL & ELECTRONIC)
CLAVE: A
- AUTORES: Tassadaq Hussain, Miquel Pericas and Eduard Ayguade
TITULO: Reconfigurable Memory Controller with Programmable Pattern Support
REVISTA/LIBRO: 5th HiPEAC Workshop on Reconfigurable Computing (WRC 2011). Heraklion, Crete, January 2011
CLAVE: Conf

- AUTORES: Muhammad Shafiq, Miquel Pericas and Eduard Ayguade
 TITULO: A Template System for the Efficient Compilation of Domain Abstractions onto Reconfigurable Computers
 REVISTA/LIBRO: 5th HiPEAC Workshop on Reconfigurable Computing (WRC 2011). Heraklion, Crete, January 2011
 CLAVE: Conf
- AUTORES: Harm Munk, Eduard Ayguadé, Cédric Bastoul, Paul Carpenter, Zbigniew Chamski, Albert Cohen, Marco Cornero, Philippe Dumont, Marc Duranton, Mohammed Fellahi, Roger Ferrer, Razya Ladelsky, Menno Lindwer, Xavier Martorell, Cupertino Miranda, Dorit Nuzman, Andrea Ornstein, Antoniu Pop, Sebastian Pop, Louis-Noël Pouchet, Alex Ramírez, David Ródenas, Erven Rohou, Ira Rosen, Uzi Shvadron, Konrad Trifunovic and Ayal Zaks
 TITULO: ACOTES Project: Advanced Compiler Technologies for Embedded Streaming
 REVISTA/LIBRO: International Journal on Parallel Programming, Volume 39, Number 3, pp. 397-450. Special Issue on High Performance and Embedded Architecture and Compilation. June 2011.
 DOI 10.1007/s10766-010-0132-7
 CLAVE: A
- AUTORES: Muhammad Shafiq, Miquel Pericàs, Nacho Navarro and Eduard Ayguadé
 TÍTULO: TARCAD: A Template Architecture for Reconfigurable Accelerator Designs
 REVISTA/LIBRO: 9th IEEE Symposium on Application Specific Processors (SASP), San Diego, California, June 5-6, 2011.
 CLAVE: Conf
- AUTORES: Lluc Alvarez, Ramon Bertran, Marc Gonzalez, Xavier Martorell, Nacho Navarro and Eduard Ayguadé
 TITULO: Design Space Exploration of CMPs with Caches and Local Memories
 REVISTA/LIBRO: 20th International ACM Symposium on High-performance Parallel and Distributed Computing (HPDC 2011), San Jose, California, June 8-11, 2011.
 CLAVE: Conf
- AUTORES: Javier Bueno, Luis Martinell, Alejandro Duran, Montse Farreras, Xavier Martorell, Rosa M. Badia, Eduard Ayguadé, and Jesús Labarta
 TITULO: Productive Cluster Programming with OmpSs
 REVISTA/LIBRO: Euro-Par 2011. Bordeaux, France, August 29-September 2, 2011.
 CLAVE: Conf
- AUTORES: Nikola Vujic, Felipe Cabarcas, Marc Gonzalez, Alex Ramirez, Xavier Martorell and Eduard Ayguadé
 TITULO: DMA++: On the Fly Data Realignment for On-Chip Memories
 REVISTA/LIBRO: IEEE Transactions on Computers. Doi: 10.1109/TC.2010.255. To appear, 2011.
 Impact factor: 2.61 (Q1)
 CLAVE: A
- AUTORES: Alejandro Duran, Eduard Ayguadé, Rosa M. Badia, Jesús Labarta, Luis Martinell, Xavier Martorell and Judit Planas
 TITULO: OmpSs: a Proposal for Programming Heterogeneous Multi-core Architectures
 REVISTA/LIBRO: International Journal of High Performance Computing Applications (IJHPCA). To be published, 2011.
 Impact Factor: 1.274 (Rank 21/49-Q2 in COMPUTER SCIENCE, HARDWARE & ARCHITECTURE and 47/95-Q3 in COMPUTER SCIENCE, INTERDISCIPLINARY APPLICATIONS, and 34/91-Q2 in COMPUTER SCIENCE, THEORY AND METHODS
 CLAVE: A

- AUTORES: Ramon Bertran, Yolanda Becerra, David Carrera, Vicenç Beltran, Marc Gonzalez, Xavier Martorell, Nacho Navarro, Jordi Torres and Eduard Ayguadé
TÍTULO: Energy Accounting for Shared Virtualized Environments under DVFS using PMC-based Power Models
REVISTA/LIBRO: Future Generation Computing Systems.
<http://dx.doi.org/10.1016/j.future.2011.03.007>. To appear, 2011.
CLAVE: A

- AUTORES: David Carrera, Malgorzata Steinder, Ian Whalley, Jordi Torres, Eduard Ayguadé
TÍTULO: Autonomic Placement of Mixed Batch and Transactional Workloads
REVISTA/LIBRO: IEEE Transactions on Parallel and Distributed Systems,
<<http://doi.ieeecomputersociety.org/10.1109/TPDS.2011.129>>. To appear 2011.
Impact factor: 1.733 (Rank 20/92-Q1 in COMPUTER SCIENCE, THEORY & METHODS and 62/246-Q2 in ENGINEERING, ELECTRICAL & ELECTRONIC)
CLAVE: A

**Participación en contratos de I+D de especial relevancia
con Empresas y/o Administraciones (nacionales y/o internacionales)**

- TITULO DEL CONTRATO: Diseño e Implementación de un Sistema Multiprocesador con Buses Multiplexados
EMPRESA/ADMINISTRACION FINANCIADORA: Telefónica
DURACION DESDE: Octubre 1985 HASTA: Septiembre 1986
INVESTIGADOR RESPONSABLE: J. Labarta

- TITULO DEL CONTRATO: Performance-Critical Applications of Parallel Architectures APPARC, Basic Research Action BRA 6634
EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea ESPRIT III
DURACION DESDE: Junio 1992 HASTA: Junio 1995
INVESTIGADOR RESPONSABLE: M. Valero

- TITULO DEL CONTRATO: Automatic Data Distribution for the CONVEX SPP
EMPRESA/ADMINISTRACION FINANCIADORA: CONVEX Computer Corporation and CONVEX Supercomputer S.A.E
DURACION DESDE: Noviembre 1992 HASTA: Enero 1995
INVESTIGADOR RESPONSABLE: J. Labarta (Project Manager), E. Ayguadé (Project Leader)

- TITULO DEL CONTRATO: Advanced Computer Training and Technology Transfer, ACT- UETP University Enterprises Training Partnership
EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea COMETT 92/1/7414/Ca
DURACION DESDE: Septiembre 1992 HASTA: Septiembre 1994
INVESTIGADOR RESPONSABLE: M. Valero

- TITULO DEL CONTRATO: Training in Advanced Computing Applications, Tools and Architectures, ACT-UETP University Enterprises Training Partnership
EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea COMETT 94/1/8353/Ca
DURACION DESDE: Septiembre 1994 HASTA: Diciembre 1995
INVESTIGADOR RESPONSABLE: M. Valero

- TITULO DEL CONTRATO: Integrating Data and Work Distribution in the Exemplar Systems
EMPRESA/ADMINISTRACION FINANCIADORA: CONVEX Computer Corporation and CONVEX Supercomputer S.A.E
DURACION DESDE: Enero 1995 HASTA: Julio 1995
INVESTIGADOR RESPONSABLE: J. Labarta (Project Manager), E. Ayguadé (Project Leader)

- TITULO DEL CONTRATO: PARANDES - Parallel Software for Applications in Chilean Industry
EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea Esprit, ITDC'94
DURACION DESDE: 1995 HASTA: 1997
INVESTIGADOR RESPONSABLE: M. Valero

- TITULO DEL CONTRATO: PARALIN - Parallel Computing Modelling for Industrial Problems
EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea Esprit, INCO-DC
DURACION DESDE: 1996 HASTA: 1998
INVESTIGADOR RESPONSABLE: M. Valero

- TITULO DEL CONTRATO: NANOS - Effective Integration of Fine-grain Parallelism Exploitation and Multiprogramming
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea Esprit, Long Term Research LTR 21907
 DURACION DESDE: 1996 HASTA: 1999
 INVESTIGADOR RESPONSABLE: J. Labarta
- TITULO DEL CONTRATO: INTONE – Innovative OpenMP Tools for Non-experts
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Information Society Technologies, IST-1999-20252.
 DURACION DESDE: Noviembre 2000 HASTA: Marzo 2003
 INVESTIGADOR RESPONSABLE: E. Ayguadé
- TITULO DEL CONTRATO: IBM Faculty Partnership Award “OpenMP compiler for the IBM Blue Gene chip architecture”
 EMPRESA/ADMINISTRACION FINANCIADORA: IBM International Business Machines Corporation.
 DURACION DESDE: Mayo 2001 HASTA: Abril 2002
 INVESTIGADOR RESPONSABLE: E. Ayguadé
- TITULO DEL CONTRATO: POP – Performance Portability of OpenMP
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Information Society Technologies, IST-2001-33071.
 DURACION DESDE: Diciembre 2001 HASTA: Noviembre 2004
 INVESTIGADOR RESPONSABLE: J. Labarta
- TITULO DEL CONTRATO: IBM Faculty Partnership Award “SDSM Support for Easy Management and Programming of BlueGene Light”
 EMPRESA/ADMINISTRACION FINANCIADORA: IBM International Business Machines Corporation.
 DURACION DESDE: Mayo 2003 HASTA: Abril 2004
 INVESTIGADOR RESPONSABLE: E. Ayguadé
- TITULO DEL CONTRATO: IBM Research Contract “Scalability of MPI for BG/L”
 EMPRESA/ADMINISTRACION FINANCIADORA: IBM T.J. Watson Research
 DURACION DESDE: Marzo 2003 HASTA: Febrero 2005
 INVESTIGADOR RESPONSABLE: E. Ayguadé
- TITULO DEL CONTRATO: IBM Faculty Partnership Award “Programming models for Blue Gene Light with emphasis on productivity and performance”
 EMPRESA/ADMINISTRACION FINANCIADORA: IBM International Business Machines Corporation
 DURACION DESDE: Mayo 2004 HASTA: Abril 2005
 INVESTIGADOR RESPONSABLE: E. Ayguadé
- TITULO DEL CONTRATO: BSC-IBM SOW: Cell Programming Models
 EMPRESA/ADMINISTRACION FINANCIADORA: IBM Research Watson
 DURACION DESDE: June 2006 HASTA: May 2008
 INVESTIGADOR RESPONSABLE: E. Ayguadé
- TITULO DEL CONTRATO: SARC – Scalable Computer Architecture
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Information Society Technologies, IST-2006-27648
 DURACION DESDE: January 2006 HASTA: December 2009
 INVESTIGADOR RESPONSABLE: M. Valero / X. Martorell

- TITULO DEL CONTRATO: ACOTES – Advanced Compiler Technologies for Embedded Streaming
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Information Society Technologies, IST-2006-034869
 DURACION DESDE: June 2006 HASTA: May 2009
 INVESTIGADOR RESPONSABLE: E. Ayguadé / A. Ramirez
- TITULO DEL CONTRATO: BSC-IBM SOW: Adaptive Systems
 EMPRESA/ADMINISTRACION FINANCIADORA: IBM Research Watson
 DURACION DESDE: December 2006 HASTA: November 2008
 INVESTIGADOR RESPONSABLE: J. Torres
- TITULO DEL CONTRATO: BSC-Microsoft: Transactional Memory and Multicore Systems
 EMPRESA/ADMINISTRACION FINANCIADORA: Microsoft
 DURACION DESDE: Enero 2007 HASTA: December 2008
 INVESTIGADOR RESPONSABLE: M. Valero
- TITULO DEL CONTRATO: VELOX -- An Integrated Approach to Transactional Memory on Multi- and Many-core Computers
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Information Society Technologies, IST-2007-216852
 DURACION DESDE: Enero 2008 HASTA: December 2010
 INVESTIGADOR RESPONSABLE: M. Valero
- TITULO DEL CONTRATO: BSC-IBM MareIncognito project
 EMPRESA/ADMINISTRACION FINANCIADORA: IBM Research
 DURACION DESDE: Julio 2007 HASTA: Junio 2011
 INVESTIGADOR RESPONSABLE: J. Labarta
- TITULO DEL CONTRATO: HiPEAC-2 Network of Excellence (High-performance and Embedded Architectures and Compilers)
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Computing Systems, FP7/ICT 217068
 DURACION DESDE: Febrero 2008 HASTA: Enero 2012
 INVESTIGADOR RESPONSABLE: M. Valero
- TITULO DEL CONTRATO: HPC-Europa2, Pan-European Research Infrastructure on High-Performance Computing, JRA2 HPC on Massively Parallel Architectures: Programming models
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Research Infrastructures Initiative, contract no. 228398
 DURACION DESDE: Enero 2009 HASTA: Diciembre 2010
 INVESTIGADOR RESPONSABLE: J. Labarta
- TITULO DEL CONTRATO: Programming models and tools for heterogeneous multicore systems
 EMPRESA/ADMINISTRACION FINANCIADORA: US Air Force, contract num. FA8655-09-1-3075
 DURACION DESDE: Junio 2009 HASTA: Mayo 2010
 INVESTIGADOR RESPONSABLE: J. Labarta
- TITULO DEL CONTRATO: TERAFLUX -- Exploiting Dataflow Parallelism in Teradevice Computing
 EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Information Society Technologies, IST-2007-249013
 DURACION DESDE: January 2010 HASTA: December 2013
 INVESTIGADOR RESPONSABLE: M. Valero

- TITULO DEL CONTRATO: EnCORE -- ENabling technologies for a programmable many-CORE
EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Information Society Technologies, IST-2007-248647
DURACION DESDE: March 2010 HASTA: February 2013
INVESTIGADOR RESPONSABLE: A. Ramirez

- TITULO DEL CONTRATO: TEXT – Towards Exaflop applicaTions
EMPRESA/ADMINISTRACION FINANCIADORA: Comunidad Económica Europea, Information Society Technologies, IST-2007-261580
DURACION DESDE: June 2010 HASTA: May 2013
INVESTIGADOR RESPONSABLE: J. Labarta

Patentes y Modelos de utilidad

INVENTORES: M. Valero, M. Peiron y E. Ayguadé
TITULO: Sincronización del Acceso a la Memoria de Multiprocesadores Vectoriales
NUM. DE REGISTRO: P-9401905
AÑO: 1994
ENTIDAD TITULAR: Universitat Politècnica de Catalunya
PAISES: España

INVENTORES: Eduard Ayguade, Tong Chen, Alexandre E. Eichenberger, Marc Gonzalez, Xavier Martorell, John K. O'Brien, Kathryn M. O'Brien, Zehra N. Sura and Tao Zhang
TITULO: Data Transfer Optimized Software Cache for REGULAR Memory References
NUM. DE REGISTRO: Serial No.: 057447, Series Code:12
AÑO: March 28, 2008 (filed)
ENTIDAD TITULAR: INTERNATIONAL BUSINESS MACHINES CORPORATION
PAISES: USA

INVENTORES: Eduard Ayguade, Tong Chen, Alexandre E. Eichenberger, Marc Gonzalez, Xavier Martorell, John K. O'Brien, Kathryn M. O'Brien, Zehra N. Sura and Tao Zhang
TITULO: Data Transfer Optimized Software Cache for IRREGULAR Memory References
NUM. DE REGISTRO: Serial No.: 057456, Series Code:12
AÑO: March 28, 2008 (filed)
ENTIDAD TITULAR: INTERNATIONAL BUSINESS MACHINES CORPORATION
PAISES: USA

Estancias en Centros extranjeros

CLAVE: D = doctorado, P = postdoctoral, I = invitado, C = contratado, O = otras (especificar).

CENTRO: Center for Supercomputing Research and Development (CSR), University of Illinois at Urbana-Champaign
LOCALIDAD: Urbana (Illinois) PAIS: USA AÑO: 1994 DURACIÓN: 3 semanas
TEMA: Compiladores para Supercomputadores CLAVE: I

Contribuciones a Congresos

- TITULO: Experiences with Automatic Data Distribution
TIPO DE PARTICIPACION: Conferencia Invitada
CONGRESO: Convex User Group Worldwide Conference 1994
LUGAR DE CELABRACION: Dallas-Texas (USA)
AÑO: 1994
- TITULO: Automatic Data Distribution for the CONVEX Exemplar
TIPO DE PARTICIPACION: Conferencia Invitada
CONGRESO: European Convex User Conference 1994
LUGAR DE CELABRACION: Krakow, Poland
AÑO: 1994
- TITULO: High Performance Fortran Languages and Basic Compilation Strategies
TIPO DE PARTICIPACION: Tutorial
CONGRESO: SUP'EUR95 High Performance Computing in Europe
LUGAR DE CELABRACION: Madrid
AÑO: 1995
- AUTORES: Mateo Valero y Eduard Ayguadé
TITULO: Arquitecturas y Compiladores para Computadores Paralelos
TIPO DE PARTICIPACION: Tutorial
CONGRESO: XV International Conference of the Chilean Computer Science Society
LUGAR DE CELABRACION: Arica - Chile
AÑO: 1995
- AUTORES: E. Ayguadé, C. Barrado, A. González, J. Labarta, D. López, J. Llosa, S. Moreno, D. Padua, F. Reig and M. Valero
TITULO: Ictineo: A Tool for Research on ILP
TIPO DE PARTICIPACION: Poster en Research Exhibit "The Polaris Compiler: Use in Research and Education".
CONGRESO: IEEE/ACM Supercomputing'96
LUGAR DE CELEBRACIÓN: Pittsburgh (PA)
AÑO: 1996
- TITULO: Arquitecturas Paralelas y Modelos de Programación
TIPO DE PARTICIPACION: Tutorial
CONGRESO: XVII International Conference of the Chilean Computer Science Society
LUGAR DE CELABRACION: Valparaiso - Chile
AÑO: 1997
- TITULO: NanosCompiler: A Research Infrastructure for Multilevel Parallelism Exploitation in OpenMP
TIPO DE PARTICIPACION: Invited talk
CONGRESO: Waseda University Seminar
LUGAR DE CELEBRACION: Tokyo - Japan
AÑO: 1999
- TITULO: Programming Models for New Architectures.
TIPO DE PARTICIPACION: Invited talk
CONGRESO: Seminar at the Universidad de la Laguna
LUGAR DE CELEBRACION: Canary Islands (Spain)
AÑO: 2000

- TITULO: Self-analysis in Parallel Program Scheduling
 TIPO DE PARTICIPACION: Invited talk
 CONGRESO: IBM conference on Shaping the Information Society in Europe – 2002.
 LUGAR DE CELEBRACION: Berlin (Germany)
 AÑO: 2002.
- TITULO: OpenMP Implementation and Performance Issues, Eduard Ayguade (CEPBA), Mats Brorsson (KTH), Sven Karlsson (KTH), Xavier Martorell (CEPBA) and Marc Gonzalez (CEPBA)
 TIPO DE PARTICIPACION: Tutorial
 CONGRESO: Fourth European Workshop on OpenMP (EWOMP 2002).
 LUGAR DE CELEBRACION: Rome (Italy)
 AÑO: 2002.
- TITULO: Performance Analysis Tools and Techniques
 TIPO DE PARTICIPACION: Panel session organizer
 CONGRESO: Fourth European Workshop on OpenMP (EWOMP 2002).
 LUGAR DE CELEBRACION: Rome (Italy)
 AÑO: 2002.
- TITULO: What are the necessary ingredients of scalable OpenMP programming?
 TIPO DE PARTICIPACION: Panel session member
 CONGRESO: European Workshop on OpenMP EWOMP'2003
 LUGAR DE CELEBRACION: Aachen (Germany)
 AÑO: 2003
- TITULO: Experiences in Exploiting Nested Parallelism in OpenMP
 TIPO DE PARTICIPACION: Invited talk
 CONGRESO: Workshop on OpenMP: Experiences and Implementations WOMPEI'2003
 LUGAR DE CELEBRACION: Tokyo (Japan)
 AÑO: 2003
- TITULO: Programación de Arquitecturas Paralelas
 TIPO DE PARTICIPACION: Invited talk
 CONGRESO: Summer School at the Universidad de Castilla-La Mancha. July 7-9, 2004.
 LUGAR DE CELEBRACION: Albacete (Spain)
 AÑO: 2004
- TITULO: Arquitecturas y Modelos de Programación para Multicore
 TIPO DE PARTICIPACION: Invited talk
 CONGRESO: Jornadas de Paralelismo. September 17, 2008
 LUGAR DE CELEBRACION: Castellon de la Plana (Spain)
 AÑO: 2008

Tesis Doctorales dirigidas

- TITULO: Extracció Automàtica de Paral·lelisme en Bucles Seqüencials Numèrics amb Recurrencies
DOCTORANDO: Jordi Torres i Viñals
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 1993
- TITULO: Optimització del Rendiment del Sistema de Memòria en Multiprocessadors Vectorials
DOCTORANDO: Montse Peiron Guàrdia (Co-direcció con Mateo Valero)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 1995
- TITULO: Reducing the Impact of Register Pressure on Software Pipelining
DOCTORANDO: Josep Llosa Espuny (Co-direcció con Mateo Valero)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 1996
- TITULO: Distribución Automática de Datos en MPP
DOCTORANDO: Jordi Garcia Alminyana (Co-direcció con Jesús Labarta)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 1997
- TITULO: Organization and Compiler Management of Register Files
DOCTORANDO: Javier Zalamea León (Co-direcció con Josep Llosa)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 2002
- TITULO: Memory Instruction Bypassing
DOCTORANDO: Daniel Ortega Fernandez (Co-direcció con Mateo Valero)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 2003
- TITULO: Multilevel Parallelism Exploitation in Shared-memory Multiprocessor Systems
DOCTORANDO: Marc Gonzalez Tallada (Co-direcció con Xavier Martorell)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 2003
- TITULO: Performance Improvement of Multithreaded Java Applications Execution on Multiprocessor Systems
DOCTORANDO: Jordi Guitart (Co-direcció con Jordi Torres)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 2005
- TITULO: Adaptive Execution Environments for Application Servers
DOCTORANDO: David Carrera Perez (Co-direcció con Jordi Torres)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 2008

- TITULO: Improving Web Server Efficiency on Commodity Hardware
DOCTORANDO: Vicenç Beltran Querol (Co-direcció con Jordi Torres)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 2008

- TITULO: Self-tuned Parallel Runtimes: a Case of Study for OpenMP
DOCTORANDO: Alejandro Duran Gonzalez (Co-direcció con Julita Corbalan)
UNIVERSIDAD: Universitat Politècnica de Catalunya
FACULTAD/ESCUELA: Departament d'Arquitectura de Computadors
AÑO: 2008

Experiencia en organización de actividades de I+D

Organización de congresos, seminarios, jornadas, etc., científicos-tecnológicos

- TIPO DE PARTICIPACION: Comité Organizador
CONGRESO: 1^{as} Jornadas de Paralelismo
LUGAR/FECHA: Sitges, Barcelona (10/1990)
- TIPO DE PARTICIPACION: Comité Organizador, Comité de Programa y Presidencia de Sesión
CONGRESO: 2nd International Workshop on Massive Parallelism: Hardware, Software and Applications
LUGAR/FECHA: Capri, Italy (10/1994)
- TIPO DE PARTICIPACION: Comité Organizador (Local Arrangements and Registration Chair) y Presidencia de Sesión
CONGRESO: 9th ACM International Conference on Supercomputing (ICS'95)
LUGAR DE CELABRACION: Barcelona, Spain (7/95)
AÑO: 1995
- TIPO DE PARTICIPACION: Comité de Programa
CONGRESO: XV International Conference of the Chilean Computer Science Society (SCCC'95)
LUGAR DE CELABRACION: Arica, Chile (11/95)
AÑO: 1995
- TIPO DE PARTICIPACION: Comité de Programa
CONGRESO: 8th IEEE Symposium on Parallel and Distributed Processing (SPDP'96)
LUGAR DE CELABRACION: New Orleans, USA (10/96)
AÑO: 1996
- TIPO DE PARTICIPACION: Chair del Steering Committee y Comité Organizador
CONGRESO: 3rd Workshop on Automatic Data Layout and Performance Prediction (AP'97)
LUGAR DE CELABRACION: Barcelona, Spain (1/97)
AÑO: 1997
- TIPO DE PARTICIPACION: Comité Organizador (Local Arrangements and Registration Chair)
CONGRESO: 25th International Symposium on Computer Architecture (ISCA'98)
LUGAR DE CELABRACION: Barcelona, Spain (6/98)
AÑO: 1998
- TIPO DE PARTICIPACION: Comité de Programa y Comité Organizador (Publicity Chair)
CONGRESO: 13th International Conference on Supercomputing (ICS'99)
LUGAR DE CELABRACION: Rhodes, Greece (6/99)
AÑO: 1999
- TIPO DE PARTICIPACION: Presidencia de Sesión
CONGRESO: 29th Annual International Conference on Parallel Processing (ICPP'99)
LUGAR DE CELABRACION: Aizu, Japan (9/99)
AÑO: 1999
- TIPO DE PARTICIPACION: Comité Organizador (Publicity Chair)
CONGRESO: 14th International Conference on Supercomputing (ICS'00)
LUGAR DE CELABRACION: Santa Fe, New Mexico (5/2000)
AÑO: 2000

- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 30th Annual International Conference on Parallel Processing (ICPP'00)
 LUGAR DE CELABRACION: Toronto, Canada (8/2000)
 AÑO: 2000
- TIPO DE PARTICIPACION: Comité Organizador
 CONGRESO: 4th International Symposium on High-Performance Computing (ISHPC2K)
 LUGAR DE CELABRACION: Tokyo, Japan (10/2000)
 AÑO: 2000
- TIPO DE PARTICIPACION: Comité Organizador (Program Chair)
 CONGRESO: Workshop on OpenMP: Experiences and Implementations (WOMPEI part of ISHPC2K)
 LUGAR DE CELABRACION: Tokyo, Japan (10/2000)
 AÑO: 2000
- TIPO DE PARTICIPACION: Comité de Programa y Comité Organizador (Publicity Chair)
 CONGRESO: 7th International Conference on High-Performance Computing (HiPC'00)
 LUGAR DE CELABRACION: Bangalore, India (12/2000)
 AÑO: 2000
- TIPO DE PARTICIPACION: Comité de Programa, Presidencia de Keynote y Participación en Panel Session.
 CONGRESO: 2nd European Workshop on OpenMP (EWOMP'00)
 LUGAR DE CELABRACION: Univ. of Edimburgh, Scotland - UK (9/2000)
 AÑO: 2000
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: Compilers and Operating Systems for Low Power (PACT'00)
 LUGAR DE CELABRACION: Philadelphia (PA), (10/2000)
 AÑO: 2000
- TIPO DE PARTICIPACION: Comité Organizador (Publicity Chair)
 CONGRESO: 15th International Conference on Supercomputing (ICS'01)
 LUGAR DE CELABRACION: Sorrento, Italy (5/2000)
 AÑO: 2001
- TIPO DE PARTICIPACION: Program Vice-chair
 CONGRESO: EUROPAR'01 (Instruction-level Parallelism and Architecture Track)
 LUGAR DE CELABRACION: Manchester, UK (8/2001)
 AÑO: 2001
- TIPO DE PARTICIPACION: Comité Organizador (Program Committee Web Master Chair) y Presidencia de Sesión
 CONGRESO: 30th Annual International Conference on Parallel Processing (ICPP'01)
 LUGAR DE CELABRACION: Valencia, Spain (9/2001)
 AÑO: 2001
- TIPO DE PARTICIPACION: Comité Organizador (Local Arrangements Co-chair y Conference Web Master)
 CONGRESO: 10th Parallel Architectures and Compilation Techniques (PACT'01)
 LUGAR DE CELABRACION: Barcelona, Spain (9/2001)
 AÑO: 2001
- TIPO DE PARTICIPACION: General Chair
 CONGRESO: 3rd European Workshop on OpenMP (EWOMP'01)
 LUGAR DE CELABRACION: Barcelona, Spain (9/2001)
 AÑO: 2001

- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: Workshop on Compilers and Operating Systems for Low Power (COLP'01)
 LUGAR DE CELABRACION: Barcelona, Spain (9/2001)
 AÑO: 2001
- TIPO DE PARTICIPACION: Program Chair
 CONGRESO: 2nd Workshop on OpenMP: Experiences and Implementations (WOMPEI'02 part of ISHPC-02)
 LUGAR DE CELABRACION: Kyoto, Japan (5/2002)
 AÑO: 2002
- TIPO DE PARTICIPACION: Comité Organizador (Publicity Co-chair) y Comité de Programa
 CONGRESO: 16th International Conference on Supercomputing (ICS'02)
 LUGAR DE CELABRACION: NY, USA (6/2002)
 AÑO: 2002
- TIPO DE PARTICIPACION: Comité Organizador (Publicity Co-chair) y Comité de Programa
 CONGRESO: 31st Annual International Conference on Parallel Processing (ICPP'02)
 LUGAR DE CELABRACION: Vancouver, Canada (8/2002)
 AÑO: 2002
- TIPO DE PARTICIPACION: Comité Organizador (Program Vice-chair)
 CONGRESO: EUROPAR'02 (Compilers for High-Performance / Compilation and Parallelization Techniques - Track)
 LUGAR DE CELABRACION: Paderborn, Germany (8/2002)
 AÑO: 2002
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 11th Parallel Architectures and Compilation Techniques (PACT'02)
 LUGAR DE CELABRACION: Virginia, USA (9/2002)
 AÑO: 2002
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: Second Workshop on Caching, Coherence, and Consistency (WC3'02), part of PACT'02
 LUGAR DE CELABRACION: Virginia, USA (9/2002)
 AÑO: 2002
- TIPO DE PARTICIPACION: Organizador y Chair de Panel Session
 CONGRESO: Fourth European Workshop on OpenMP (EWOMP 2002).
 LUGAR DE CELEBRACION: Rome, Italy (18-20/9/2002)
 AÑO: 2002.
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 2003 International Parallel and Distributed Processing Symposium (IPDPS).
 LUGAR DE CELEBRACION: Nice, France (22-26/4/2003)
 AÑO: 2003
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 8th International Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'2003), in conjunction with International Parallel and Distributed Processing Symposium (IPDPS-2003).
 LUGAR DE CELEBRACION: Nice, France (22/4/2003)
 AÑO: 2003
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: WOMPAT 2003: Workshop on OpenMP Applications and Tools.
 LUGAR DE CELEBRACION: Toronto, Canada (26-27/6/2003)
 AÑO: 2003

- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: First International Workshop on Heterogeneous and Adaptive Computing - "Challenges of Large Applications in Distributed Environments" (CLADE 2003), in conjunction with 12th IEEE International Symposium on High Performance Distributed Computing (HPDC-12).
 LUGAR DE CELEBRACION: Seattle, Washington, USA (21/6/2003)
 AÑO: 2003
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: Fifth European Workshop on OpenMP (EWOMP 2003).
 LUGAR DE CELEBRACION: Aachen, Germany (22-26/9/2003)
 AÑO: 2003
- TIPO DE PARTICIPACION: Program Vice-chair del track "Compilers and Languages"
 CONGRESO: 32nd Annual International Conference on Parallel Processing (ICPP'03)
 LUGAR DE CELEBRACION: Kaohsiung, Taiwan (6-9/10/2003)
 AÑO: 2003
- TIPO DE PARTICIPACION: General Co-Chair
 CONGRESO: 3rd Workshop on OpenMP: Experiences and Implementations (WOMPEI'03 part of ISHPC-V)
 LUGAR DE CELEBRACION: Tokyo, Japan (20-22/10/2003)
 AÑO: 2003
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 9th International Workshop on High-Level Parallel Programming Models and Supportive Environments (HIPS'2004), in conjunction with International Parallel and Distributed Processing Symposium (IPDPS-2004).
 LUGAR DE CELEBRACION: Santa Fe, USA (26/4/2004)
 AÑO: 2004
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: WOMPAT 2004: Workshop on OpenMP Applications and Tools.
 LUGAR DE CELEBRACION: Houston, USA.(6/2004)
 AÑO: 2004
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: Second International Workshop on Heterogeneous and Adaptive Computing - "Challenges of Large Applications in Distributed Environments" (CLADE 2004), in conjunction with 13th IEEE International Symposium on High Performance Distributed Computing (HPDC-12).
 LUGAR DE CELEBRACION: Honolulu, Hawaii (7/6/2004)
 AÑO: 2004
- TIPO DE PARTICIPACION: Comité Organizador (Publicity co-chair) y Comité de Programa
 CONGRESO: 33rd Annual International Conference on Parallel Processing (ICPP'04)
 LUGAR DE CELEBRACION: Montreal, Canada (15-18/8/2004)
 AÑO: 2004
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 13rd Parallel Architectures and Compilation Techniques (PACT'04)
 LUGAR DE CELEBRACION: Antibes Juan-les-Pins, France (29/9-3/10/2004)
 AÑO: 2004
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 15th International Conference on Application-specific Systems, Architectures and Processors (ASAP'04)
 LUGAR DE CELEBRACION: Galveston TX, USA (27-29/9/2004)
 AÑO: 2004

- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: EWOMP 2004: European Workshop on OpenMP.
 LUGAR DE CELEBRACION: Stockholm, Sweden (18-22/10/2004)
 AÑO: 2004
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 3rd Workshop on OpenMP: Experiences and Implementations (WOMPEI'05)
 LUGAR DE CELEBRACION: Tsukuba, Japan (20-21/1/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Program Vice-Chair del track "Compilers and Languages"
 CONGRESO: 34th Annual International Conference on Parallel Processing (ICPP'05)
 LUGAR DE CELEBRACION: Oslo, Norway (14-17/6/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 1st International Workshop on OpenMP (IWOMP-2005)
 LUGAR DE CELEBRACION: Oregon, USA (1-4/6/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 10th IEEE Symposium on Computers and Communications (ISCC'05)
 LUGAR DE CELEBRACION: Cartagena, Spain (27-30/6/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Publicity co-Chair
 CONGRESO: 14th Parallel Architectures and Compilation Techniques (PACT'05)
 LUGAR DE CELEBRACION: Saint Louis, Missouri (17-21/9/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: The 2005 International Conference on High-Performance Computing and Communications (HPCC-2005)
 LUGAR DE CELEBRACION: Sorrento, Naples (Italy) (21-24/9/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: General/Program co-Chair
 CONGRESO: 18th International Workshop on Languages and Compilers for Parallel Computing. (LCPC-2005)
 LUGAR DE CELEBRACION: New Orleans, Louisiana (20-22/10/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 10th Asia-Pacific Computer Systems Architecture Conference (ACSAC05)
 LUGAR DE CELEBRACION: Singapore (24-16/10/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 3rd Int. Symposium on Parallel and Distributed Processing and Applications (ISPA'05)
 LUGAR DE CELEBRACION: Nanjing , China , (2-5/11/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Local Arrangements Chair y Comité de Programa
 CONGRESO: 38th International Symposium on Microarchitecture (MICRO-38)
 LUGAR DE CELEBRACION: Barcelona, Spain (12-16/11/2005)
 AÑO: 2005

- TIPO DE PARTICIPACION: Local Arrangements Chair y Comité de Programa
 CONGRESO: 2005 International Conference on High-Performance Embedded Architectures and Compilers (HiPEAC 2005)
 LUGAR DE CELABRACION: Barcelona, Spain (17-18/11/2005)
 AÑO: 2005
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 14th Euromicro Conference on Parallel, Distributed and Network-based Processing (PDP2006)
 LUGAR DE CELABRACION: Montbeliard-Sochaux, France (15-17/2/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 20th IEEE International Parallel & Distributed Processing Symposium (IPDPS 2006)
 LUGAR DE CELABRACION: Rhodes, Greece (25-29/4/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 2nd International Workshop on OpenMP (IWOMP-2006)
 LUGAR DE CELABRACION: Reims, France (12-15/6/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: The 20th ACM International Conference on Supercomputing (ICS 2006)
 LUGAR DE CELABRACION: Queensland, Australia (28/6 – 1/7/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Issue Global Chair
 CONGRESO: Issue 7: Parallel Computer Architecture and ILP. Euro-Par 2006 European Conference on Parallel Computing
 LUGAR DE CELABRACION: Dresden, Germany (29/8 – 1/9/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: The 2006 International Conference on High Performance Computing and Communications (HPCC 2006)
 LUGAR DE CELABRACION: Munich, Germany (13-15/9/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: Workshop on Programming Models for Ubiquitous Parallelism (in conjunction with PACT'06)
 LUGAR DE CELABRACION: Seattle, Washington (16/9/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: CASCON 2006: Dublin Symposium, In association with the 16th Annual International Conference on Computer Science and Software Engineering
 LUGAR DE CELABRACION: Dublin, Ireland (16-17/10/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 19th International Workshop on Languages and Compilers for Parallel Computing (LCPC 2006)
 LUGAR DE CELABRACION: New Orleans, Louisiana (USA) (2-4/11/2006)
 AÑO: 2006

- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 13th IEEE International Conference on High Performance Computing (HiPC 2006)
 LUGAR DE CELABRACION: Bangalore, India (18-21/12/2006)
 AÑO: 2006
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 15th Euromicro International Conference on Parallel, Distributed and Network-based Processing (PDP 2007)
 LUGAR DE CELABRACION: Naples, Italy (7-9/2/2007)
 AÑO: 2007
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP'07)
 LUGAR DE CELABRACION: San Jose (CA), USA (14-17/3/2007)
 AÑO: 2007
- TIPO DE PARTICIPACION: Publications Co-Chair y Comité de Programa
 CONGRESO: 3rd International Workshop on OpenMP (IWOMP-2007)
 LUGAR DE CELABRACION: Beijing, China (3-7/6/2007)
 AÑO: 2007
- TIPO DE PARTICIPACION: Program Co-chair
 CONGRESO: 21st ACM International Conference on Supercomputing (ICS'07)
 LUGAR DE CELABRACION: Seattle area, USA (7/2007)
 AÑO: 2007
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 20th International Workshop on Languages and Compilers for Parallel Computing (LCPC 2007)
 LUGAR DE CELABRACION: University of Illinois at Urbana-Champaign, Chicago (USA) (11-13/10/2007)
 AÑO: 2007
- TIPO DE PARTICIPACION: Comité Organizador
 CONGRESO: MULTIPROG-2008 – First Workshop on Programmability Issues for Multi-Core Computers. Held in conjunction with the 3rd International Conference on High-Performance Embedded Architectures and Compilers (HiPEAC)
 LUGAR DE CELABRACION: Goteborg, Sweden (1/2008)
 AÑO: 2008
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: CC 2008 – International Conference on Compiler Construction
 LUGAR DE CELABRACION: Budapest, Hungary (4/2008)
 AÑO: 2008
- TIPO DE PARTICIPACION: Publications Co-Chair y Comité de Programa
 CONGRESO: 4th International Workshop on OpenMP (IWOMP-2008)
 LUGAR DE CELABRACION: Purdue University, West Lafayette, IN (USA) (12-14/5/2008)
 AÑO: 2008
- TIPO DE PARTICIPACION: Workshops and Tutorials Chair
 CONGRESO: 22nd ACM International Conference on Supercomputing (ICS'08)
 LUGAR DE CELABRACION: Island of Kos, Greece (7-12/6/2008)
 AÑO: 2008

- TIPO DE PARTICIPACION: Organizing Committee
 CONGRESO: BMW-2008 – 1st Barcelona Multicore Workshop
 LUGAR DE CELABRACION: Barcelona, Spain (5-6/6/2008)
 AÑO: 2008
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 21st International Workshop on Languages and Compilers for Parallel Computing (LCPC 2008)
 LUGAR DE CELABRACION: Edmonton, Alberta (Canada) (31/7-2/8/2008)
 AÑO: 2008
- TIPO DE PARTICIPACION: Comité de Programa (Compilers and Languages Track)
 CONGRESO: ICPP 2008 – International Conference on Parallel Processing
 LUGAR DE CELABRACION: Portland (OR), USA (8-12/9/2008)
 AÑO: 2008
- TIPO DE PARTICIPACION: Comité Organizador
 CONGRESO: MULTIPROG-2009 –Second Workshop on Programmability Issues for Multi-Core Computers. Held in conjunction with the 4th International Conference on High-Performance Embedded Architectures and Compilers (HiPEAC 2009)
 LUGAR DE CELABRACION: Paphos, Cyprus, (25-28/1/2009)
 AÑO: 2009
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: HiPEAC 2009 – The 4th International Conference on High Performance and Embedded Architectures and
 LUGAR DE CELABRACION: Paphos, Cyprus, (25-28/1/2009)
 AÑO: 2009
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 5th International Workshop on OpenMP (IWOMP-2009)
 LUGAR DE CELABRACION: Technische Universität Dresden (Germany) (3-5/6/2009)
 AÑO: 2009
- TIPO DE PARTICIPACION: Program Committee and Web/Submissions Chair
 CONGRESO: 23rd ACM International Conference on Supercomputing (ICS'09)
 LUGAR DE CELABRACION: Yorktown Heights, NY, USA (8-12/6/2009)
 AÑO: 2009
- TIPO DE PARTICIPACION: “Multicore and Manycore Programming” Topic Vice-chair
 CONGRESO: EuroPar 2009
 LUGAR DE CELABRACION: Delft (The Netherlands) (25-28/8/2009)
 AÑO: 2009
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 22nd International Workshop on Languages and Compilers for Parallel Computing (LCPC 2009)
 LUGAR DE CELABRACION: University of Delaware, Newark, Delaware (USA) (8-10/10/2009)
 AÑO: 2009
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: International Conference on Parallel and Distributed Systems (ICPADS'09)
 LUGAR DE CELABRACION: Shenzhen, China (9-11/12/2009)
 AÑO: 2009

- TIPO DE PARTICIPACION: Comité Organizador
 CONGRESO: MULTIPROG-2010 – Third Workshop on Programmability Issues for Multi-Core Computers. Held in conjunction with the 5th International Conference on High-Performance Embedded Architectures and Compilers (HiPEAC 2010)
 LUGAR DE CELABRACION: Pisa, Italy, (24/1/2010)
 AÑO: 2010
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 6th International Workshop on OpenMP (IWOMP-2010)
 LUGAR DE CELABRACION: U. of Tsukuba, (14-16/6/2010)
 AÑO: 2010
- TIPO DE PARTICIPACION: Program Committee and Web/Submissions Chair
 CONGRESO: 24th ACM International Conference on Supercomputing (ICS'10)
 LUGAR DE CELABRACION: Tsukuba, Japan (1-4/6/2010)
 AÑO: 2010
- TIPO DE PARTICIPACION: Program Committee
 CONGRESO: Workshop on Parallel Programming and Applications on Accelerator Clusters (PPAAC), held in conjunction with IEEE Cluster 2010
 LUGAR DE CELABRACION: Heraklion (Crete), Greece (20/9/2010)
 AÑO: 2010
- TIPO DE PARTICIPACION: Scientific Committee and Local Organizer
 CONGRESO: BMW-2010 – 2nd Barcelona Multicore Workshop
 LUGAR DE CELABRACION: Barcelona, Spain (21-22/10/2010)
 AÑO: 2010
- TIPO DE PARTICIPACION: Program Committee
 CONGRESO: HiPC 2010 – High Performance Computing Conference
 LUGAR DE CELABRACION: Goa, India (19-22/12/2010)
 AÑO: 2010
- TIPO DE PARTICIPACION: Workshops and Tutorials Co-chair
 CONGRESO: HiPEAC 2011 – 6th International Conference on High-Performance Embedded Architectures and Compilers
 LUGAR DE CELABRACION: Heraklion (Crete), Greece (22-26/1/2011)
 AÑO: 2011
- TIPO DE PARTICIPACION: Comité Organizador
 CONGRESO: MULTIPROG-2010 – Third Workshop on Programmability Issues for Multi-Core Computers. Held in conjunction with the 5th International Conference on High-Performance Embedded Architectures and Compilers (HiPEAC 2010)
 LUGAR DE CELABRACION: Heraklion (Crete), Greece (23/1/2011)
 AÑO: 2011
- TIPO DE PARTICIPACION: Comité de Programa
 CONGRESO: 7th International Workshop on OpenMP (IWOMP-2011)
 LUGAR DE CELABRACION: Chigaco (USA) (13-15/6/2011)
 AÑO: 2011
- TIPO DE PARTICIPACION: Topic Vice-chair
 CONGRESO: Euro-Par 2011. Topic 11: Multicore and Manycore Programming
 LUGAR DE CELABRACION: Bordeaux (France) (29/8/2011-2/9/2011)
 AÑO: 2011

- TIPO DE PARTICIPACION: Program Committee
CONGRESO: ICPADS 2011: 17th IEEE International Conference on Parallel and Distributed Systems, "Multicore Computing and Parallel/Distributed Architecture" track
LUGAR DE CELABRACION: Tainan, Taiwan (7-9/12/2011)
AÑO: 2011

- TIPO DE PARTICIPACION: Program Committee
CONGRESO: HiPC 2011: 18th International Conference on High Performance Computing
LUGAR DE CELABRACION: Bengaluru (Bangalore), India (19-22/12/2011)
AÑO: 2011

Otros méritos o aclaraciones que se desee hacer constar

- Beca de Telefónica a jornada completa durante el curso académico 1985-86 para la realización del Proyecto Fin de Carrera "Diseño e Implementación de un Sistema Multiprocesador con Buses Multiplexados".
- Tesis doctoral: Paralelización de Recurrencias en Programas Secuenciales Numéricos. Universitat Politècnica de Catalunya. ISBN: 84-689-1738-9. Versión electrónica: <http://www.tdx.cesca.es/TDX-0207105-094858>.
- Premio Extraordinario de Doctorado "Doctor en Informática 1989", otorgado por la Universitat Politècnica de Catalunya (23/9/92).
- Responsable de las peticiones de ayuda para la organización del congreso ICS'95: "Programa Sectorial de Promoción del Conocimiento" (Ministerio de Educación y Ciencia) por un valor de 900.000 Ptas, "Accions Mobilitzadores" (CIRIT-1994) por un valor de 200.000 Ptas y "Accions Mobilitzadores" (CIRIT-1995) por un valor de 550.000 Ptas.
- Responsable de las peticiones de ayuda para la organización del congreso ISCA'98: "Acciones Especiales TIC" (Ministerio de Educación y Ciencia, 1998) por un valor de 1.500.000 Ptas y "Accions Mobilitzadores 1998ARCS" (CIRIT-1998) por un valor de 300.000 Ptas.
- Responsable de las peticiones de ayuda para la organización del congreso PACT'2001 y EWOMP'2001: "Acciones Especiales TIC" (Ministerio de Educación y Cultura, TIC2000-2884-E) por un valor de 1.400.000 Ptas. y "Ajuts per a l'Organització de Congressos i Reunions de la UPC, 2000-CO0024) per un valor de 300.000 Ptas.
- Referee habitual de artículos en revistas internacionales (IEEE Transactions on Computers, IEEE Transactions on Parallel and Distributed Processing, Journal of Parallel and Distributed Computing, Journal of Programming Languages, Scientific Programming, Parallel Processing Letters, etc.).
- Member of the Editorial Board. International Journal of Computational Science and Engineering. InderScience Publishers. Since 2005.
- Member of the Editorial Board. International Journal of Parallel Programming. Springer. Since 2006.
- Referee de ponencias en congresos internacionales (ACM Int. Symposium on Computer Architecture ISCA, IEEE/ACM International Symposium on Microarchitecture MICRO, ACM Int. Conference on Supercomputing ICS, IEEE High-Performance Computer Architecture HPCA, IEEE Symposium on Parallel and Distributed Processing SPDP, International Workshop on Massive Parallelism, CONPAR, PARLE, EURO-PAR, International Conference on Parallel Processing ICPP, IEEE International Parallel and Distributed Processing Symposium IPDPS, etc.).
- Responsable de Formación del C⁴ (Centre de Computació i Comunicacions de Catalunya), desde su creación en Enero de 1996 hasta Diciembre 1997.
- Responsable de Tercer Ciclo y Programa de Doctorado del Departament d'Arquitectura de Computadors de la Universitat Politècnica de Catalunya, desde Octubre de 1997 hasta Septiembre de 1999 y desde Febrero de 2003 hasta abril del 2005.
- Subdirector del Departament d'Arquitectura de Computadors de la Universitat Politècnica de Catalunya, desde Mayo de 2005 hasta Junio de 2006.
- Miembro de la Comisión de Tercer Ciclo de la Universitat Politècnica de Catalunya, desde Septiembre de 2000 hasta Septiembre de 2004.
- Dirección de más de 12 Proyectos Fin de Carrera (PFC) en las titulaciones de Ingeniería de Telecomunicaciones de Barcelona (ETSETB) e Ingeniería en Informática de Barcelona (FIB).

- (Co)Director de las tesis doctorales que actualmente están realizando: Paul Carpenter, Vladimir Gajinov, Nikola Vujic, Muhammad Shafiq y Judit Planas.
- Mercedor de un IBM Faculty Partnership Award para los años 2001, 2003 y 2004, con un importe de \$40.000 cada uno.
- Miembro del Board of Directors de cOMPunity, Inc. (The Community of OpenMP Users, Researchers, Tool Developers and Providers), desde Noviembre 2001. Representante de cOMPunity en el Futures committee del OpenMP ARB (Architecture Review Board), desde Septiembre 2001.
- Miembro del Board of Managers y Project Manager and Researcher del CEPBA-IBM Research Institute (Barcelona), desde Noviembre de 2000 hasta Diciembre 2004.
- Miembro del SUSP (Scientific Users' Selection Panel) del programa de movilidad (Transnational Access) del proyecto HPC-Europa (Pan-European Research Infrastructure on High Performance Computing), desde 2004.
- Director asociado del Departamento de Computer Sciences del Barcelona Supercomputing Center (BSC), desde abril del 2005.